

## LIMITS FOR AUTOMATIC VERIFICATION OF FINITE-STATE CONCURRENT SYSTEMS

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In one of the most exciting developments in the theory of program correctness, Clarke and Emerson [1] and Queille and Sifakis [4] developed systems which automatically check whether a given finite-state concurrent program satisfies a given temporal formula. These systems allow us to verify the correctness of various nontrivial concurrent programs automatically. For example, automatic verification of correctness of the alternating bit protocol on the system of [2] took just over 19 seconds of CPU time.

An obvious limitation of these systems comes from the fact that only finite-state programs can be checked for correctness. Although many interesting concurrent programs are in fact finite-state, they are often given schematically in terms of a parameter  $n$ , representing the number of concurrent processes. Such a schematic program really represents an infinite sequence of uniformly defined finite-state programs. Consider, for example, solutions to the mutual exclusion problem. A solution for  $n$  processes competing for access to a critical section is to be correct for any value of  $n \geq 2$ . To check correctness of such a solution using the systems of [1,2,4] (provided, for each  $n \geq 2$ , the solution uses only a finite number of states), one would have to verify the solutions for

2 processes, 3 processes, etc. obtained from the schematic one by plugging in the values 2, 3, etc. for  $n$ . This cannot be done in finite time. An obvious question is whether the systems of [1,2,4] can be extended to handle schematic programs.

We show in this short article that in general such an extension cannot exist. The proof of this fact is obvious and the main difficulty lies rather in a satisfactory formalization of the problem. In subsequent considerations we abstract from the particular representation of the finite-state programs and from the particular form of the temporal logic considered. All functions considered are unary unless stated otherwise.

Let  $P, \phi$  denote (codes of) total recursive functions which for each  $n$  produce a finite-state program  $P(n)$  and a temporal formula  $\phi(n)$  in a given temporal logic, respectively. We call the pair  $(P, \phi)$  a *verification problem*.

A typical example of a verification problem is the following (incomplete) solution to the mutual exclusion problem for  $n$  processes:  $P(n)$  is the schematic program  $[P_1 \parallel \dots \parallel P_n]$  where for  $i = 1, 2, \dots, n$ ,  $P_i$  is the program

```
zi := i;
while true do
  while i ≠ min{j | zj = j} do skip od;
  CSi; /* critical section */
  zi := n
od
```

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and  $\phi(n)$  is the temporal formula

$$\bigwedge_{1 \leq i \leq n} z_i = 0 \rightarrow \bigwedge_{1 \leq i < j \leq n} \square \neg (at CS_i \wedge at CS_j).$$

Here,  $CS_i$  for  $i = 1, 2, \dots, n$  is a further unspecified finite-state program which does not modify  $z_1, \dots, z_n$ . Observe that, for each  $n$ ,  $P(n)$  is a finite-state program satisfying  $\phi(n)$ .

Let  $R$  be any recursive set of pairs of codes  $(P, \phi)$  of total recursive functions containing all pairs of finite-state transducers. Let  $Q_R$  be the relation

$$Q_R(P, \phi) \Leftrightarrow \begin{cases} \forall n (P(n) \text{ satisfies } \phi(n)) & \text{if } (P, \phi) \in R, \\ \text{false} & \text{otherwise.} \end{cases}$$

We now show that  $Q_R$  is not semi-decidable. In fact, the following stronger result holds.

**Theorem.** *Any relation  $Q_R$  is  $\Pi_1^0$ -complete (in the terminology of Rogers [5]).*

**Proof.** By the results of [1,2,4] there exists an algorithm whose running time is polynomial in the number of states and the length of the temporal formula which allows us to decide whether a given finite-state program satisfies a given temporal formula. (Clarke et al. [1,2,4] use branching time logic, but the Theorem holds for any temporal logic for which this validity problem is decidable.) Thus,  $Q_R$  is in  $\Pi_1^0$ . To show that  $Q_R$  is  $\Pi_1^0$ -hard, let  $M$  be an arbitrary Turing machine with no input, and let  $P(n)$  be the following finite-state program:

```

begin
  flag := false;
  for  $i := 1$  to  $n$  do
    simulate one step of  $M$ 
  od;
  if  $M$  has not yet halted then flag := true
end
    
```

Then the final value of the Boolean variable *flag* is *true* iff  $M$  has not halted within  $n$  steps. Thus,

$$\begin{aligned} \neg \text{HALT}(M) &\Leftrightarrow \forall n P(n) \text{ satisfies the formula} \\ &\quad \square (at \text{ end} \rightarrow flag) \\ &\Leftrightarrow (P, \square (at \text{ end} \rightarrow flag)) \in Q_R, \end{aligned}$$

where  $\square$  stands for the usual ‘always’ operator of linear temporal logic. We can also write

$$\begin{aligned} \neg \text{HALT}(M) &\Leftrightarrow \forall n P(n) \text{ satisfies the formula } \diamond flag \\ &\Leftrightarrow (P, \diamond flag) \in Q_R, \end{aligned}$$

where  $\diamond$  stands for the usual ‘eventually’ operator of linear temporal logic (see, e.g., [3]).

Under a suitable encoding,  $P$  can be computed by a finite-state transducer, since it need only attach a bounded integer counter to a description of  $M$ ; and the function  $\phi$  is constant. This proves the Theorem, since  $\neg \text{HALT}$  is  $\Pi_1^0$ -complete.  $\square$

Note that we have in fact proved that there is a fixed formula  $\phi$  such that the relation  $Q_R(\cdot, \phi)$  is  $\Pi_1^0$ -complete.

The proof does not make use of parallelism; for each  $n$ ,  $P(n)$  is a sequential program. By using instead of  $P(n)$  a parallel program  $[P(n) \parallel \dots \parallel P(n)]$  consisting of  $n$  identical components, we get the same result addressing parallel programs explicitly.

The Theorem shows that even very simple temporal properties of schematic finite-state programs are not semi-decidable. Clearly, the result does not depend on the logic chosen. Indeed, for the case of deterministic programs, linear and branching time temporal logics coincide. The program properties considered in the proof can also be expressed in Hoare’s logic, dynamic logic, or any other reasonable for formalism appropriate for studying program correctness.

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