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(54) **METHOD AND SYSTEM FOR TESTING THE OPERATION OF A TARGET COMPUTER SYSTEM BY USING TIMED REQUIREMENTS**

VERFAHREN UND SYSTEM ZUM TESTEN DES BETRIEBS EINES ZIELRECHNERSYSTEMS UNTER VERWENDUNG VON ZEITGESTEUERTEN ANFORDERUNGEN

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Description**TECHNICAL FIELD**

5 **[0001]** This disclosure relates to requirement engineering for computer systems and relates more specifically to a method and system for testing the operation of a target computer system by using timed requirements.

BACKGROUND ART

10 **[0002]** In the process of developing computer systems, requirement engineering consists in defining, documenting and maintaining the requirements that a computer system needs to comply with. Requirements can be of different nature but, in computer systems, timed requirements are of importance. Basically, a timed requirement corresponds to constraints, including timing constraints, between outputs of the computer system and inputs to said computer system. In practice, timed requirements will be the basis of the design and development of the computer system. Also, timed requirements will partly drive the testing campaign in order to check that they are indeed complied by the actual computer system, once developed.

15 **[0003]** Of course, it is of primary importance to design timed requirements that are consistent, i.e. that are not contradictory such that implementations conforming with said timed requirements are indeed possible and may be designed and developed.

20 **[0004]** In the scientific literature, there exist different consistency notions defined for real-time systems' timed requirements.

[0005] For instance, real-time consistency (a.k.a. rt-consistency) [PHP11] requires that all finite execution sequences that do not already violate some timed requirement have an extension that satisfies all of them. Put differently, this means that if an implementation produces a finite execution sequence whose all continuations necessarily lead to the violation of some timed requirement, then there must be a timed requirement that is already violated by the finite execution sequence. Thus, rt-consistency ensures that the set of timed requirements is well designed and sane. This notion of consistency is interesting in that it may catch subtle problems in the set of timed requirements, but it is rather computationally expensive to check.

25 **[0006]** Another example corresponds to partial consistency [Bec19] (see also [LDWH19] and, for general constraints, [JJNV89]), which is a weaker notion of consistency that only considers conflicts between pairs of timed requirements. In practice, partial consistency checking is usually restricted to execution sequences of bounded length, but it may still detect important flaws in the specifications of the timed requirements. The general motivation is to gain in efficiency by restricting to pairs of timed requirements, but also by focusing on situations where inconsistencies may arise. The computational complexity for checking partial consistency is lower than for checking rt-consistency, even if it can remain computationally expensive in some cases. However, one should be aware that focusing on pairs might introduce false negatives (considering that the set is not inconsistent while it is inconsistent), since consistency of pairs of requirements does not imply consistency of the whole set timed requirements, since inconsistencies can be introduced by adding a new timed requirement. Conversely, focusing on pairs might also produce false positives (considering the set is inconsistent while it is consistent), since an inconsistency of a pair of requirements can be removed by adding a new timed requirement of the set which prevents the execution sequence revealing the inconsistency to occur.

SUMMARY

30 **[0007]** The present disclosure aims at proposing a solution for checking the consistency of a set of timed requirements describing the operation of a target computer system which is computationally less expensive than rt-consistency checking and provides a more accurate assessment of the consistency of the set of timed requirements than (bounded) partial consistency checking.

[0008] The invention is defined in detail in the appended independent claims, whereas preferred embodiments of the invention are defined in the appended dependent claims.

35 **[0009]** According to a first aspect, the present disclosure relates to a computer implemented method for testing the operation of a target computer system, said operation of the target computer system being constrained by a set of timed requirements, wherein said method comprises defining subsets of timed requirements of the set, and for each subset of a plurality of subsets, iterating steps of:

- 40
- 45 - searching for an execution sequence of the target computer system, referred to as witness trace, satisfying a criterion for detecting a conflict between timed requirements of the subset;
 - when a witness trace is found: searching for a contradicting timed requirement among the timed requirements of the set which are not present in the subset, which prevents the witness trace to occur when considered with the subset;

- when a contradicting timed requirement is found: augmenting the subset by including the contradicting timed requirement in the subset;

wherein an inconsistency of the operation of the target computer system constrained by the set of timed requirements is detected when a witness trace is found for at least one subset and no contradicting timed requirement is found for said witness trace.

[0010] Hence, the testing method aims at evaluating the operation of a target computer system. The target computer system may be any computer system for which timed requirements may be defined, such as an embedded electronic system and/or mechatronic system and/or a mechanical control system, etc.

[0011] The operation of the target computer system is constrained by a set of timed requirements. Testing the operation of the target computer system corresponds to simulating the behavior of the target computer system at the timed requirement level, in order to detect inconsistencies among the timed requirements. Hence, it is emphasized that the testing method does not interact with the actual target computer system, which may not be designed or developed yet, but evaluates the operation of the target computer system by simulating its behavior at the timed requirement level. Accordingly, the testing method is carried out by a testing computer system that is different from the target computer system.

[0012] As discussed above, a timed requirement is may viewed as constraints, including timing constraints, between outputs of the target computer system and inputs to said target computer system. A timed requirement may be modeled by a set of possible states of the target computer system, with at least one error state, together with possible transitions between states and the associated timing constraints.

[0013] The testing method searches for execution sequences of the target computer system, referred to as "witness trace", that may lead to conflicts between timed requirements, i.e. that may lead to at least one timed requirement being violated. An execution sequence basically corresponds a sequence of inputs to the target computer system and resulting outputs from said target computer system at successive time steps. At least one timed requirement is violated when the constraints it defines cannot be satisfied at some point during an execution sequence. It should be noted that the witness trace itself does not violate a timed requirement yet. However, the witness trace is not prevented to occur by the considered timed requirements and yet it reaches a configuration of the target computer system from which at least one timed requirement will be violated at some point.

[0014] The testing method searches for witness traces satisfying a criterion for detecting a conflict between timed requirements. Basically, the conflict detection criterion may use different notions of inconsistency checking, such as rt-inconsistency checking or partial inconsistency checking. Detailed examples of conflict detection criteria are provided hereinbelow.

[0015] The testing method searches for witness traces in an iterative manner, by searching for witness traces in subsets of the original set. Basically, a witness trace is searched for in a subset of the set of timed requirements, i.e. the witness trace complies with the timed requirements of the subset in that it is not prevented to occur by said timed requirements, but it reaches a configuration of the target computer system from which at least one timed requirement of the subset will be violated at some point.

[0016] If a witness trace for a subset is found (i.e. an inconsistency of the subset is detected), then a further check on the witness trace is performed by searching for a contradicting timed requirement in the remaining timed requirements of the set, i.e. in the timed requirements of the set that are not present in the considered subset. Basically, a contradicting timed requirement corresponds to a timed requirement which prevents the witness trace to occur when considered with the considered subset. Hence the contradicting timed requirement is a timed requirement that is violated by the witness trace before the end of the witness trace. In other words, if a contradicting timed requirement is found for the witness trace, this means that the inconsistency detected in the considered subset by the witness trace is in fact a false positive, that is therefore discarded.

[0017] When such a contradicting timed requirement is found, then it is added to the considered subset, and the testing method resumes to searching for a witness trace in the augmented subset. When no contradicting timed requirement is found, then the inconsistency detected is confirmed.

[0018] An inconsistency of the set of timed requirements is detected when a witness trace is found for at least one subset and no contradicting timed requirement is found for said witness trace. It should be noted that, if the subset has augmented up to a size N_{TR} , then the subset is identical to the original set and all the timed requirements are included in the subset, such that if a witness trace is found, it is not possible to find a contradicting timed requirement.

[0019] It is possible to consider that no inconsistency is detected when no witness traces are found for the subsets.

[0020] When augmenting the subsets, it is possible to set a higher bound on the size of the subset. Enhanced accuracy may be achieved if the higher bound is set to the number N_{TR} of timed requirements of the set. However, it is also possible to consider a higher bound K_{TRmax} lower than N_{TR} in order to control the computational complexity required for searching for witness traces in a subset.

[0021] This iterative process allows to quickly find inconsistencies after a few iterations. Inconsistencies often depend

on several timed requirements but not on all of them. While previous approaches required considering all timed requirements at once (which does not scale), or they would only consider pairs of timed requirements (which might lead to false positives or false negatives).

5 [0022] The present solution checks several subsets of timed requirements of growing size, which proves more efficient from a computational standpoint, and enables detecting more inconsistencies while discarding false positives. Indeed, increasing the size of a subset is not systematic and is only required when a witness trace is found for the subset and a false positive is detected.

[0023] In specific embodiments, the testing method can further comprise one or more of the following features, considered either alone or in any technically possible combination.

10 [0024] In specific embodiments, the conflict detection criterion is expressed using Computation Tree Logic, CTL, and searching for a witness trace uses a model checking tool. The advantage of CTL model checking is to be able to check a complex conflict detection criterion (expressed in CTL) rather than designing a specific algorithm for the task in hand. Moreover, this check can be done efficiently for timed automata with Boolean variables, by using e.g. Binary Decision Diagrams, BDD, based model checking.

15 [0025] In specific embodiments, searching for a witness trace uses a Satisfiability Modulo Theories, SMT, solver. Such SMT solvers are efficient in finding variable assignments satisfying given constraints. They prove very efficient in bounded model checking, which consists in checking traces of bounded length, and thus searching for witness traces of bounded length.

[0026] In specific embodiments, each timed requirement is modeled as a timed automaton. Timed automata can model complex timed requirements and can be checked efficiently using state-of-the-art model checkers.

20 [0027] In specific embodiments, each timed requirement is modeled as a Simplified Universal Pattern, SUP. SUPs are a restricted class of timed requirements, for which computationally efficient specific consistency checking tools exist.

[0028] According to a second aspect, the present disclosure relates to a computer program product comprising instructions which, when executed by at least one processor, configure said at least one processor to carry out a testing method according to any one of the embodiments of the present disclosure.

25 [0029] According to a third aspect, the present disclosure relates to a computer-readable storage medium comprising instructions which, when executed by at least one processor, configure said at least one processor to carry out a testing method according to any one of the embodiments of the present disclosure.

[0030] According to a fourth aspect, the present disclosure relates to a testing computer system for testing the operation of a target computer system, said testing computer system comprising at least one processor configured to carry out a testing method according to any one of the embodiments of the present disclosure.

BRIEF DESCRIPTION OF DRAWINGS

35 [0031] The invention will be better understood upon reading the following description, given as an example that is in no way limiting, and made in reference to the figures which show:

- Figure 1: a schematic representation of a timed requirement modeled as a Simplified Universal Pattern, SUP;
- Figure 2: a schematic representation of an exemplary timed requirement modeled as a timed automaton;
- 40 - Figure 3: a schematic representation of two exemplary timed requirements modeled as timed automata;
- Figure 4: a schematic representation illustrating the interference between action phases of two different SUP timed requirements after their trigger phases;
- Figure 5: a diagram representing the main steps of an exemplary embodiment of a testing method.

45 [0032] In these figures, references identical from one figure to another designate identical or analogous elements. For reasons of clarity, the elements shown are not to scale, unless explicitly stated otherwise.

DESCRIPTION OF EMBODIMENTS

50 [0033] As discussed above, the present disclosure relates to a method 50 and system for testing the operation of a target computer system by searching for inconsistencies between timed requirements of a set of timed requirements the target computer system must comply with.

[0034] We first introduce some definitions and conventions used in the following non-limitative description, and then we present the main steps of the testing method and different preferred embodiments of the testing method 50.

A. Definitions

55 [0035] We consider target computer systems (such as Programmable Logic Controllers or PLCs) whose behaviors

are driven by the observation of Boolean variables and are subject to discrete timing constraints. Intuitively, a run of the target computer system can be understood as a sequence of time steps at which the value of Boolean variables is observed, the evolution of the target computer system being governed by both timing constraints and the Boolean value of these Boolean variables.

5 **[0036]** For this kind of target computer systems, timed requirements can for example be represented by timed automata such as those described in [AD90], with specific restrictions, but extended with Boolean variables. In subsection A.1, we introduce a formal definition of deterministic discrete timed automata with Boolean variables. In subsection A.2, we discuss execution sequences of a set of timed requirements. In subsection A.3, we introduce the notion of Simplified Universal Patterns (SUP) [Bec19] and show how timed requirements can be represented by this form of timed automata.

10 **A.1. Timed automata**

[0037] Timed automata (TA) [AD90] extend finite-state automata with variables, called clocks, that can be used to measure (and impose constraints on) delays between various events along executions. More precisely, given a set
15 $\mathcal{X} = \{c_i \mid 1 \leq i \leq k\}$ of clocks, the set of allowed constraints is defined using the following grammar:

$$20 \quad \mathcal{C}(\mathcal{X}) \ni g ::= c \sim n \mid g \wedge g$$

where c ranges over \mathcal{X} , n ranges over \mathbb{N} , \sim ranges over $\{<, \leq, =, \geq, >\}$, and the operator \wedge corresponds to the logical "AND". We consider integer-valued clocks. The semantics of such clock constraints may be defined as follows: given a clock valuation $v: \mathcal{X} \rightarrow \mathbb{N}$, that a constraint
25

$$g \in \mathcal{C}(\mathcal{X})$$

holds true at v , denoted $v \models g$, is defined inductively as follows:

30 $v \models g \sim n$ if, and only if, $v(c) \sim n$
 $v \models g_1 \wedge g_2$ if, and only if, $v \models g_1$ and $v \models g_2$.

35 **[0038]** For a valuation, an integer $d \in \mathbb{N}$, and a subset $R \subseteq \mathcal{X}$, we let:

$$40 \quad \begin{array}{ll} v + d: \mathcal{X} \rightarrow \mathbb{N} & v[R \leftarrow 0]: \mathcal{X} \rightarrow \mathbb{N} \\ c \mapsto v(c) + d & c \in R \mapsto 0 \\ & c \notin R \mapsto v(c) \end{array}$$

[0039] Finally, we let 0 be the valuation mapping all variables to 0.

45 **[0040]** In our timed automata, we also consider the observation of the evolution of the target computer system through the values of Boolean variables. We thus consider a set $AP = \{b_i \mid 1 \leq i \leq n\}$ of atomic propositions, and we define the set

of Boolean constraints $\mathcal{B}(AP)$ as the set of all propositional formulas built on AP . Notice that those Boolean variables cannot be modified, but are only observed, since they belong to the target computer system itself, contrary to clocks that can be reset since they are owned by the timed requirement only and used by it to check timing constraints.

50 **[0041]** A timed automaton is a tuple

$$55 \quad \mathcal{T} = \langle S, S_0, AP, \mathcal{X}, T, E \rangle$$

where S is a finite set of states, $S_0 \subseteq S$ is a set of initial states, AP is a finite set of atomic propositions, \mathcal{X} is a finite

set of clocks,

$$T \subseteq S \times \mathcal{B}(AP) \times \mathcal{C}(X) \times 2^X \times S$$

5 is a finite set of transitions, and $E \subseteq S$ is a subset of error states from which one cannot escape, i.e. for any $t = (s, c, g, r, s') \in T$, if $s \in E$ then also $s' \in E$.

10 **[0042]** For a transition $t = (s, c, g, r, s') \in T$ of a timed automaton, we define **src**(t) = s , **tgt**(t) = s' , **bool**(t) = c , **guard**(t) = g , and **reset**(t) = r . Basically, $t = (s, c, g, r, s')$ denotes a transition from **src**(t) = s to **tgt**(t) = s' that is available if the valuation of Boolean variables satisfies the boolean guard **bool**(t) = c and the valuation of clock variables satisfies the clock guard **guard**(t) = g . After the transition t , the value of each clock contained in the subset of clock variables **reset**(t) = r is reset to 0. Note that guards are pairs of Boolean and timed guards that can be interpreted (and will be noted) as conjunctions since the two types of guard do not interfere. Intuitively, error states are entered when the timed requirement is violated, and this is definitive. All other states can be considered accepting, meaning that no violation has been detected yet. This will be formalized later.

15 **[0043]** A configuration of a timed automaton is a pair (s, v) where $s \in S$ and $v \in \mathbb{N}^X$. With a timed automaton \mathcal{T} , we associate the infinite-state automaton

$$20 \quad \mathcal{S}(\mathcal{T}) = \langle Q, Q_0, \Sigma, D, \Omega \rangle$$

where:

- 25 - the set of states Q contains all configurations $(s, v) \in S \times \mathbb{N}^X$;
- the initial states are obtained by adjoining the null valuation (all clocks are mapped to zero) to initial states S_0 , i.e. $Q_0 = S_0 \times 0$;
- $\Sigma = 2^{AP}$ is the alphabet of actions, i.e. valuations of all Boolean variables;
- transitions in D are combinations of a transition of the timed automaton and a 1-time-unit delay. Formally, given a letter $\sigma \in \Sigma$ and two configurations (s, v) and (s', v') , there is a transition $((s, v), \sigma, (s', v'))$ in D if, and only if, there is a transition (s, c, g, r, s') in T such that:

◦

$$35 \quad \sigma|_c = c \text{ and } v|_g = g;$$

◦

$$40 \quad v' = (v[r \leftarrow 0] + 1);$$

- $\Omega = E \times \mathbb{N}^X$ is the set of error configurations.

45 **[0044]** A run of T is a run of its associated infinite-state automaton

$$\mathcal{S}(\mathcal{T}).$$

50 It can be represented as a sequence along which configurations and actions alternate:

$$(s_0, v_0) \cdot \sigma_1 \cdot (s_1, v_1) \cdot \sigma_2 \cdots (s_n, v_n) \cdots$$

55 **[0045]** An execution sequence (or trace) of this sequence is its projection on the set of actions. In other terms, it is a finite or infinite sequence $\sigma = \{\sigma_i\}_{0 \leq i \leq l}$ of actions where $l \in \mathbb{N} \cup \{+\infty\}$ is the length of σ , denoted with $|\sigma|$. Finite traces belong to Σ^* and infinite ones to Σ^ω . A trace is accepted by T if there is a run on that trace that never enters any error state.

[0046] It should be noted that the transition system

$$\mathcal{S}(\mathcal{T})$$

is infinite because we impose no bound on the values of the clocks during executions. However, as is classical in the setting of timed automata [AD90], we may notice that the exact value of a clock is irrelevant as soon as it exceeds the largest integer constant with which it is compared. We could thus easily modify the definition of

$$\mathcal{S}(\mathcal{T})$$

in such a way that it only contains finitely many states.

[0047] In the sequel, we consider in a non-limitative manner that the timed automata are complete, meaning that from any (reachable) configuration (s, v) , and for any subset σ of AP , there is a transition $t = (s, c, g, r, s')$ in T such that $\sigma \models c$ and $v \models g$. This is no loss of generality since we may have self-loops if we want to let more time elapse, and we may add the missing transitions to the error states. Also, the timed automaton are considered to be deterministic, i.e. for any two configurations (a, c, g, r, s) and (a, c', g', r', s') (hence starting from the same state a) such that both $c \wedge c'$ and $g \wedge g'$ are satisfiable, then $s = s'$ and $r = r'$.

[0048] In the sequel, we will need to consider several timed requirements, each of them specified by a timed automaton.

To this aim, we define the product of timed automata. Formally, given two timed automata $\mathcal{T}_1 = \langle S_1, S_{1,0}, AP_1, \mathcal{X}_1, T_1, E_1 \rangle$ and

$$\mathcal{T}_2 = \langle S_2, S_{2,0}, AP_2, \mathcal{X}_2, T_2, E_2 \rangle$$

with disjoint clock sets (i.e., $\mathcal{X}_1 \cap \mathcal{X}_2 = \emptyset$), their product

$$\mathcal{T}_1 \otimes \mathcal{T}_2$$

is a timed automaton $\mathcal{T} = \langle S, S_0, AP, \mathcal{X}, T, E \rangle$ where $S = S_1 \times S_2$, $S_0 = S_{1,0} \times S_{2,0}$, $AP = AP_1 \cup AP_2$, $\mathcal{X} = \mathcal{X}_1 \cup \mathcal{X}_2$, $E = E_1 \times S_2 \cup S_1 \times E_2$, and the set of transitions is defined as follows: there is a transition $((s_1, s_2), c, g, r, (s'_1, s'_2))$ in T if there are transitions $(s_1, c_1, g_1, r_1, s'_1)$ in T_1 and $(s_2, c_2, g_2, r_2, s'_2)$ in T_2 with $c = c_1 \wedge c_2$, $g = g_1 \wedge g_2$, and $r = r_1 \cup r_2$.

[0049] Note that in this definition, clocks of factor timed automata are disjoint, while atomic propositions are not, which may create interferences in guards of the product, and possibly inconsistencies. Also note that the product falls in an error state of E as soon as one of its factors does in E_1 or E_2 , and if, as assumed above, no transition is leaving E_1 and E_2 , this assumption is preserved on E . This product of timed automata can be easily generalized to an arbitrary number

of timed automata, and timed requirements that they specify. For a set $\mathcal{R} = \{R_i\}_{i \in I}$ of timed requirements, each specified by a timed automaton

$$\mathcal{T}_i(R_i),$$

we note $\otimes \mathcal{R}$ the timed requirement specified by the timed automaton

$$\otimes_{i \in I} \mathcal{T}_i(R_i).$$

A.2. Execution sequences

[0050] In the remainder of the present description, we consider, in a non-limitative manner, complete deterministic timed automata with error states that cannot be escaped. Notice that those properties are preserved when composing several timed automata. We use timed automata to encode timed requirements. Intuitively, entering an error state of a timed automaton corresponds to violating the corresponding timed requirement.

[0051] In the sequel, for any timed automaton \mathcal{T} and any finite or infinite execution sequence (also referred to as "trace") σ , we write

$$\sigma \models \mathcal{T}$$

(" σ satisfies \mathcal{T} ") if, and only if, σ does not violate \mathcal{T} , i.e. running σ on \mathcal{T} does not visit E.

[0052] It should be noticed that for a finite trace σ ,

$$\sigma \models \mathcal{T}$$

simply means that the timed requirement has not been violated, but it could be the case that all infinite continuations of the trace σ inevitably lead to E. To distinguish this case, we define the notation \approx for finite traces, which is stronger than \models . Hence, for a finite trace σ , and a timed automaton \mathcal{T} , we write

$$\sigma \approx \mathcal{T}$$

if there exists an infinite trace σ' such that

$$\sigma \cdot \sigma' \models \mathcal{T}.$$

[0053] Those configurations that are not yet erroneous but inevitably lead to an error state (i.e. those configurations reached by some finite traces σ such that

$$\sigma \models \mathcal{T}$$

but

$$\sigma \not\approx \mathcal{T}$$

) are all configurations InevE that satisfy the Computation Tree Logic, CTL, formula $\neg \text{err} \wedge \text{AFerr}$ where err is the proposition that is true in E and false otherwise. Configurations in InevE witness the inconsistency of the timed automaton. In fact, the timed automaton should already have detected that there is no possible continuation satisfying the timed requirement and it should already be in an error state.

[0054] For a given trace σ , and set of timed automata

$$\mathcal{R} = \{\mathcal{T}_i\}_{i \in I},$$

we write $\sigma \models \mathcal{R}$ (resp. $\sigma \approx \mathcal{R}$) to mean that $\sigma \models \otimes \mathcal{R}$ (resp. $\sigma \approx \otimes \mathcal{R}$). It should be noted that, given $\mathcal{R}' \subseteq \mathcal{R}$, then for any finite trace σ , if $\sigma \models \mathcal{R}$, then $\sigma \models \mathcal{R}'$ and if $\sigma \approx \mathcal{R}$ then $\sigma \approx \mathcal{R}'$.

A.3. Simplified Universal Patterns (SUP)

[0055] In this subsection, we describe a pattern language called Simplified Universal Pattern (SUP) [Bec19] that can be used to define timed requirements. Compared to timed automata, SUPs offer a more intuitive though less expressive way of writing timed requirements. All SUPs can be written as timed automata, but not all timed automata can be written using SUPs. We consider both timed automata and SUP requirements since SUPs have been used previously, and we propose some specific embodiments that hold only for SUPs, and some specific embodiments that hold more generally for timed automata, etc.

[0056] SUP instances can be denoted by:

$$(TSE, TC, TEE)[Tmin, Tmax] \xrightarrow{[Lmin, Lmax]} (ASE, AC, AEE)[Amin, Amax]$$

[0057] A SUP instance is thus defined by the choice of the Boolean formulas TSE, TC, TEE, ASE, AC, AEE, and durations Tmin, Tmax, Lmin, Lmax, Amin, Amax. The Boolean formulas are propositional formulas built from system Boolean variables in a set AP of atomic propositions. For simplicity, when talking about an individual SUP, we gather the values of these parameters in a vector P , and refer to its elements using the names of the parameters (e.g. P_{TSE} is the value of the first parameter TSE). But we should remember that they are valuations of formulas, and when several SUPs are considered, since they may refer to some common Boolean variables, their valuations may interfere on those variables.

[0058] Figure 1 illustrates the intuitive semantics of SUP. Intuitively, the left part of a SUP defines a trigger phase, while the right part is the action phase. For a trigger phase to be realized, TSE ("Trigger Start Event") has to be true, and then confirmed within a duration in $[Tmin, Tmax]$. Confirmation means that TC ("Trigger Condition") holds until TEE ("Trigger End Event") occurs. Otherwise the trigger is aborted.

[0059] If the trigger phase is realized, for the SUP to be satisfied, its action phase should be started by ASE ("Action Start Event") within $[Lmin, Lmax]$ time units, and then confirmed. That an action phase is realized is defined as for triggers, with a confirmation duration within $[Amin, Amax]$, during which both AC ("Action Condition") holds until AEE ("Action End Event") occurs. Otherwise, the SUP is violated.

[0060] Figure 2 represents schematically an example of deterministic timed automaton that can be used to encode a SUP timed requirements, since a SUP timed requirement is a special case of a timed automata as defined above.

[0061] As can be seen in figure 2, the timed automaton encoding the SUP timed requirement has five states idle, trig, delay, act and err (wherein err is the only error state), and an associated clock c . Also, in figure 2, the operator \vee corresponds to the logical "OR" and the operator \neg corresponds to the logical operator "NOT".

[0062] This timed automaton clearly exhibits the successive phases of a SUP. However, the real timed automaton we use to encode a SUP is the transitive closure of the timed automaton depicted on Figure 2. For instance, if both TSE and TEE hold true at the same time and $Tmin = 0$, then the transitions from idle to trig and from trig to delay have to be taken at the same time. All maximal simple (i.e. where no transitions may appear twice) sequences of transitions have to be merged this way, provided that the conjunctions of their Boolean constraints and clock guards (taking intermediary clock resets into account) can be satisfied.

[0063] Example 1. Consider the SUP $(p, p, p)[0, 0] \xrightarrow{[0, 1]} (q, q, q)[0, 0]$. In words, this SUP is triggered by a single occurrence of p , and it is satisfied if each such occurrence of p is followed, within one time unit, by an occurrence of q . For such a SUP (where $P_{TSE} = P_{TC} = P_{TEE}$, and/or where $P_{ASE} = P_{AC} = P_{AEE}$), we simplify the notations and write:

$$p[Tmin, Tmax] \xrightarrow{[Lmin, Lmax]} q[Amin, Amax]$$

[0064] We also omit to mention the intervals if they are $[0, 0]$, such that the SUP above can be written $p \xrightarrow{[0, 1]} q$, and it is satisfied e.g. along a trace where p holds true at each step and q is fulfilled at every other step.

[0065] Given a SUP requirement R , and a trace σ , we write $\sigma \models R$ (resp. $\sigma \approx R$) to mean that a timed automaton \mathcal{T} encoding R satisfies

$$\sigma \models \mathcal{T}$$

(resp.

$$\sigma|\approx\mathcal{T}$$

). In the rest of the present description, a timed requirement refers either to a timed automaton or to a SUP.

A.4. Several notions of consistency

A.4.1. Rt-Consistency

[0066] We consider the rt-consistency notion introduced in [PHP11]. As we already mentioned, consistency for a single timed automaton \mathcal{T} amounts to requiring that $\text{InevE} = E$. This asks that errors be detected as soon as they cannot be avoided. In simple terms, rt-consistency ensures that this property is true for a set of timed requirements, that is, in the product of the timed automata that encode it.

[0067] Let \mathcal{R} be a set of requirements. Then \mathcal{R} is rt-consistent if, and only if, for all finite traces σ satisfying $\sigma|\models\mathcal{R}$, we have $\sigma|\approx\mathcal{R}$.

[0068] Thus, the set \mathcal{R} is rt-consistent if all finite traces satisfying all timed requirements in \mathcal{R} (i.e. not yet violating one of the timed requirements) can be extended to an infinite trace satisfying all requirements in \mathcal{R} . In other words, for all finite traces σ .

$$\sigma|\models\mathcal{R} \Rightarrow \sigma|\approx\mathcal{R}$$

[0069] In fact, even if individual timed requirements are consistent alone ($\forall \sigma, \sigma|\models R \Rightarrow \sigma|\approx R$ for each $R \in \mathcal{R}$), this may not be the case for their conjunction. Thus, checking rt-consistency means checking the consistency of the conjunction of given timed requirements. Note also that the notion of rt-consistency is simply a generalization of consistency to a set of requirements.

[0070] Example 2. Consider the following two timed requirements: $P1: \text{request} \xrightarrow{[3, 4]} \text{response}$

$P2: \text{repair} \xrightarrow{[5, 5]} \text{!response}[3, 3]$

[0071] The first timed requirement models a target computer system that has to respond to any request within 3 to 4 time units. The second timed requirement states that if the target computer system enters a maintenance phase, then it will be off (and cannot respond) after 5 time units, and for a duration of 3 time units.

[0072] Figure 3 represents the timed automata encoding those two SUPs.

[0073] As can be seen in figure 3, the timed automaton encoding the timed requirement $P1$ has three states I_1, D_1 and E_1 (wherein E_1 is the only error state), and an associated clock c_1 . The timed automaton encoding the timed requirement $P2$ has four states I_2, D_2, A_2 and E_2 (wherein E_2 is the only error state), and an associated clock c_2 . These timed requirements are not rt-consistent. Indeed, consider a finite trace σ where the repair signal is received, followed 3 time units later with a request. Then $\sigma|\models \text{SUP}(P1) \wedge \text{SUP}(P2)$. The joint runs of the automata are as follows:

$$\begin{pmatrix} I_1 \\ c_1 = 0 \\ I_2 \\ c_2 = 0 \end{pmatrix} \xrightarrow{\text{repair (+delay)}} \begin{pmatrix} I_1 \\ c_1 = 1 \\ D_2 \\ c_2 = 1 \end{pmatrix} \xrightarrow{* (+delay)} \begin{pmatrix} I_1 \\ c_1 = 2 \\ D_2 \\ c_2 = 2 \end{pmatrix} \xrightarrow{* (+delay)} \begin{pmatrix} I_1 \\ c_1 = 3 \\ D_2 \\ c_2 = 3 \end{pmatrix} \xrightarrow{\text{request (+delay)}} \begin{pmatrix} D_1 \\ c_1 = 1 \\ D_2 \\ c_2 = 4 \end{pmatrix}$$

[0074] From this last configuration, it can be checked that no continuations of this trace will avoid reaching E_1 or E_2 . Indeed, from the last configuration above, both timed automata will first loop in their current states D_1 and D_2 , reaching configuration $(D_1, c_1 = 2, D_2, c_2 = 5)$. In order to avoid visiting E_2 , the next two steps must satisfy $\neg\text{response}$, thereby reaching configuration $(D_1, c_1 = 4, A_2, c_2 = 2)$. From that configuration, we have a conflict: if response is true at the next step, the run will reach E_2 in the second timed automaton, while if response is false (i.e. $\neg\text{response}$ is true), E_1 will be reached in the first timed automaton.

[0075] Now, assume we add the following timed requirement, which expresses that no request can be received during

maintenance:

$P3: \text{repair} \rightarrow \text{!request}[5, 5]$

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[0076] This timed requirement P3 rules out the above trace, i.e. it prevents the above trace to occur, and it can be checked that the resulting set of requirements is rt-consistent.

[0077] As will be seen in section B below, checking rt-consistency is in general computationally expensive. Several possibilities can be investigated to fight against this complexity:

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- First, one can restrict the considered class of timed requirements, while still having enough expressive power for targeted applications, and this is the idea of focusing on SUP requirements for which computationally efficient tools exist;

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- Second, one can restrict consistency checking to subsets of requirements. However, one should be aware that this might introduce false negatives: consistency of a subset does not imply consistency of the whole set, since inconsistencies can be introduced by adding a new requirement. Conversely, adding requirements may also introduce false positives: an inconsistency of a subset can be removed by adding a new requirement which prohibits some behaviors revealing an inconsistency (as for requirement P3 in the example above). We will however explore this idea, and see that some additional checks are necessary to avoid these situations;

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- Third, inconsistencies typically arise in some particular risky situations where Boolean valuations conflict. Some of these situations may in particular be identified for the restricted class of SUPs, where they correspond to conflicts between action parts of SUPs. This check can also be restricted to subsets or even pairs of timed requirements. Again, one should be aware that false negatives may arise (absence of particular risky situations on pairs does not imply consistency), as well as false positives (risky situations are not necessarily rt-inconsistencies). Again, additional conditions must be checked to get conclusions.

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[0078] The present disclosure relates *inter alia* to these developments.

A.4.2. Partial consistency

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[0079] Partial consistency was introduced in [Bec19] as a partial but more efficient check to detect inconsistencies in SUP requirements. Indeed, partial consistency checking simply identifies risky situations for pairs of SUPs that could be rt-inconsistent. We will however later show (see Lemma 3) that additional checks allow to lift witnesses of partial inconsistencies to witnesses of rt-inconsistencies.

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[0080] For a SUP requirement $R = \text{SUP}(P)$, a finite trace $\sigma \in \Sigma^*$, and $j \in \mathbb{N}$, we define the predicate $\text{TriggerAt}_R(\sigma, j)$ which holds if a trigger phase was just ended at step j . Formally, let

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$$\mathcal{T}_R$$

denote a deterministic timed automaton representing R . In addition, for

45

$$\mathcal{T}_R$$

and σ , let $(s_i, v_i)_{i \in [0, |\sigma|]}$ denote the corresponding run. Then, $\text{TriggerAt}_R(\sigma, j)$ holds if, and only if, one of the following conditions holds:

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- Either $s_{j-1} = \text{trig}$, $\sigma_j \models P_{\text{TEE}}$, and $v_{j-1}(c) \geq P_{\text{Tmin}}$;
- Or $s_{j-1} = \text{idle}$, $\sigma_j \models P_{\text{TSE}} \wedge P_{\text{TEE}}$, and $P_{\text{Tmin}} = 0$;
- Or $s_{j-1} = \text{act}$, $\sigma_j \models P_{\text{AEE}} \wedge P_{\text{TSE}} \wedge P_{\text{TEE}}$, $v_{j-1}(c) \geq P_{\text{Amin}}$ and $P_{\text{Tmin}} = 0$;
- Or $s_{j-1} = \text{delay}$, $\sigma_j \models P_{\text{ASE}} \wedge P_{\text{AEE}} \wedge P_{\text{TSE}} \wedge P_{\text{TEE}}$, $v_{j-1}(c) \geq P_{\text{Lmin}}$ and $P_{\text{Amin}} = P_{\text{Tmin}} = 0$;

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[0081] Note that the first case is the main one and the easiest one to understand: $\text{TriggerAt}_R(\sigma, j)$ holds if the timed automaton was in the state trig and P_{TEE} just occurred while P_{Tmin} time was spent during the trigger phase. Thus, the

trigger phase has successfully ended. The other items treat the cases where the trigger phase ends within one step in the initial state, or simultaneously as the previous action phase is ending, and the case where both the previous action phase and the present trigger phase are done within the same step.

5 **[0082]** For SUP requirements $R_1 = \text{SUP}(P_1)$, $R_2 = \text{SUP}(P_2)$, $R = \text{SUP}(P)$, indices $j_1, j_2, k \in \mathbb{N}$, we define: **Interfere**

$${}_{(R_1, R_2)}(j_1, j_2) = j_1 + (P_1)_{\text{Lmin}} + (P_1)_{\text{Amin}} \geq j_2 + (P_2)_{\text{Lmax}}$$

$$10 \quad \wedge j_2 + (P_2)_{\text{Lmin}} + (P_2)_{\text{Amin}} \geq j_1 + (P_1)_{\text{Lmax}}$$

$$\text{Trig}_{(R_1, R_2), k}(\sigma) = \exists j_1, j_2 \in \mathbb{N}: j_1 \leq k \wedge j_2 \leq k \wedge \text{TriggerAt}_{R_1}(\sigma, j_1)$$

$$15 \quad \wedge \text{TriggerAt}_{R_2}(\sigma, j_2) \wedge \text{Interfere}_{(R_1, R_2)}(j_1, j_2)$$

20 **[0083]** **Interfere** $_{(R_1, R_2)}(j_1, j_2)$ expresses the fact that if trigger phases of R_1 and R_2 are completed at steps j_1 and j_2 respectively, then their respective potential action phases necessarily overlap. **Trig** $_{(R_1, R_2), k}(\sigma)$ means that the trigger phases of R_1 and R_2 are completed by step k and their potential action phases overlap. Figure 4 illustrates the interferences of the action phases (action₁ and action₂ in figure 4) of two timed requirements after their trigger phases (trigger₁ and trigger₂ in figure 4). Remark that if σ is a prefix of p , then **Trig** $_{(R_1, R_2), k}(\sigma)$ implies **Trig** $_{(R_1, R_2), k}(\rho)$.

25 **[0084]** Consider SUP timed requirements R_1, R_2 and a set \mathcal{R}_{inv} of SUP timed requirements. We say that R_1 and R_2 are partially consistent with respect to \mathcal{R}_{inv} if:

$$30 \quad \forall k \in \mathbb{N}, \left(\forall i \in \{1, 2\}, \exists \sigma \in \Sigma^\infty, \quad \sigma \models \mathcal{R}_{inv} \cup \{R_i\} \wedge \text{Trig}_{(R_1, R_2), k}(\sigma) \right) \\ \Rightarrow \left(\exists \sigma \in \Sigma^\infty, \sigma \models \mathcal{R}_{inv} \cup \{R_1, R_2\} \wedge \text{Trig}_{(R_1, R_2), k}(\sigma) \right) \quad (1)$$

35 **[0085]** Partial consistency requires that for all $k \geq 1$, if traces for both timed requirements can be found in which the trigger is completed within k steps, and the timed requirement satisfied, then a single trace must exist that triggers and satisfies both timed requirements. Observe that, given set \mathcal{R}_{inv} , timed requirements R_1, R_2 , a witness trace of partial inconsistency is a number $k \geq 0$ and two sequences σ_1 and σ_2 of length k , such that each σ_i triggers both R_1 and R_2 and satisfies R_i , but there are no infinite traces that trigger both R_1 and R_2 within k steps and satisfy both.

40 **[0086]** As in [Bec19], to efficiently find witnesses of partial inconsistency, we may consider the following approximation. Given bounds $\alpha, \beta > 0$, timed requirements R_1, R_2 are (α, β) -bounded partial consistent if:

$$45 \quad \forall k \leq \alpha, \left(\forall i \in \{1, 2\}, \exists \sigma \in \Sigma^\alpha, \sigma \models \mathcal{R}_{inv} \cup \{R_i\} \wedge \text{Trig}_{(R_1, R_2), k}(\sigma) \right) \\ \Rightarrow \left(\exists \sigma \in \Sigma^{\alpha+\beta}, \sigma \models \mathcal{R}_{inv} \cup \{R_1, R_2\} \wedge \text{Trig}_{(R_1, R_2), k}(\sigma) \right) \quad (2)$$

50 **[0087]** Thus, the approximation consists in looking for witness traces of bounded length for the failure of Equation (2), but remark that witness traces for the failure of Equation (2) are not witness traces of the failure of Equation (1) which require infinite traces.

55 **[0088]** Example 3. We consider again the timed requirements of Example 2. Timed requirements $P1$ and $P2$ are not partially consistent under empty \mathcal{R}_{inv} , since there are traces that trigger both timed requirements, but as soon as their action phases overlap, both cannot be fulfilled. Under timed requirement $P3$, timed requirements $P1$ and $P2$ cannot interfere, so that with $\mathcal{R}_{inv} = \text{SUP}(P3)$, those two timed requirements are partially consistent.

[0089] There are a few differences with the original definition of partial consistency of [Bec19]. First, partial consistency

of [Bec19] only checks the very first trigger of the traces, that is, the TriggerAt formula is used to define the position of the trigger of the first phase only. In our case, we consider also the triggers of later phases. In fact, **TriggerAt**(σ, j) is true whenever j is the end of the trigger of some phase of σ . Thus, we can detect more subtle inconsistencies.

[0090] The second difference is that the bounded approximation in [Bec19] checks for the existence of a lasso-shaped execution in the timed automata that recognize the SUP timed requirements. The advantage of this is that such a lasso describes an infinite execution, so if partial consistency holds, so does the bounded approximation; while the converse is not true. In our case, we do not look for a lasso in the left part of Equation (2), so this implication does not hold. We do prove, on the other hand, that rt-consistency implies Equation (2) (see Lemma 3 below).

[0091] Third, in [Bec19], \mathcal{R}_{inv} contains only a specific type of timed requirements called invariants. In our case, \mathcal{R}_{inv} is an arbitrary subset of the set of timed requirements.

B. Detailed description of the testing method

[0092] Figure 5 represents the main steps of a method 50 for testing the operation of a target computer system (not represented in the figures), the operation of said target computer system being constrained by a set \mathcal{R} of N_{TR} timed requirements, $|\mathcal{R}| = N_{TR} \geq 3$.

[0093] The testing method 50 is carried out by a testing computer system (not represented in the figures). In preferred embodiments, the testing computer system comprises one or more processors (which may belong to a same computer or to different computers) and storage means (magnetic hard disk, optical disk, electronic memory, or any computer readable storage medium) in which a computer program product is stored, in the form of a set of program-code instructions to be executed in order to implement all or part of the steps of the testing method 50. Alternatively, or in combination thereof, the testing computer system can comprise one or more programmable logic circuits (FPGA, PLD, etc.), and/or one or more specialized integrated circuits (ASIC), etc., adapted for implementing all or part of said steps of the testing method 50. In other words, the testing computer system comprises a set of means configured by software (specific computer program product) and/or by hardware (processor, FPGA, PLD, ASIC, etc.) to implement the steps of the testing method 50.

[0094] As illustrated by figure 5, the testing method 50 comprises a step S51 of defining subsets of K_{TR} timed requirements of the set \mathcal{R} , $2 \leq K_{TR} < N_{TR}$. Preferably, all subsets have initially the same number of timed requirements but, in some embodiments, the initial number of timed requirements may vary from one subset to another. The subsets are all different, none of the subsets defined is included or identical to another subset. However, a given timed requirement may be included in a plurality of subsets. Preferably, the union of all the subsets defined corresponds to the set \mathcal{R} .

[0095] Then, as illustrated in figure 5, the testing method 50 comprises additional steps which may be iterated for each subset of a plurality of subsets. Preferably, said steps are executed for all the subsets defined, however it is also possible to execute these steps only on some of the subsets.

[0096] As illustrated by figure 5, for each subset of the plurality of subsets, the following steps are iterated:

- a step S52 of searching for an execution sequence of the target computer system, referred to as "witness trace", satisfying a criterion for detecting a conflict between timed requirements of the subset;
- when a witness trace is found (reference S52a in figure 5): a step S53 of searching for a contradicting timed requirement among the timed requirements of the set \mathcal{R} which are not present in the subset, which prevents the witness trace to occur when considered with the subset;
- when a contradicting timed requirement is found (reference S53a in figure 5): a step S54 of augmenting the subset by including the contradicting timed requirement in the subset.

[0097] The step S52 of searching for a witness trace satisfying the conflict detection criterion between timed requirements of the subset corresponds to performing an inconsistency check on the subset. The conflict detection criterion may use different notions of inconsistency checking, such as rt-inconsistency checking or partial inconsistency checking, and the witness trace is an execution sequence which demonstrates the inconsistency. Also, in some cases (see e.g. subsection B.2), more than one witness trace may be found, depending e.g. on the conflict detection criterion considered.

[0098] If no witness trace is found for the considered subset (reference S52b in figure 5), then no inconsistency of the subset is detected, and the execution of the different steps S52, S53 and S54 stops for this subset.

[0099] In turn, if a witness trace is found for the considered subset (reference S52a in figure 5), then the testing method 50 continues with step S53 of searching for a contradicting timed requirement in the timed requirements of the set \mathcal{R}

which are not present in the subset.

[0100] Basically, a contradicting timed requirement corresponds to a timed requirement which prevents the witness trace to occur when considered with the considered subset. Hence the contradicting timed requirement is a timed requirement that is violated by the witness trace before any timed requirement of the considered subset is violated.

[0101] If no contradicting timed requirement is found for the witness trace (reference S53b in figure 5), then an inconsistency of the subset is detected, and the execution of the different steps S52, S53 and S54 stops for this subset.

[0102] In turn, if a contradicting timed requirement is found for this witness trace (reference S53a in figure 5), then the testing method 50 continues with step S54 of augmenting the subset with the contradicting timed requirement, which consists in including the contradicting timed requirement in the considered subset, such that the number of timed requirements of this subset is incremented.

[0103] After step S54 of augmenting the considered subset, step S52 of searching a witness is executed again on the augmented subset, and so on.

[0104] As can be seen in figure 5, the execution of the different steps S52, S53 and S54 may stop e.g. when no witness trace is found or when no contradicting timed requirement is found for a given witness trace. It should be noted that no contradicting timed requirement can be found if the considered subset has been augmented up to the size of the set \mathcal{R} , i.e. when the number of timed requirements of this subset is equal to N_{TR} and there are no timed requirements of the set which are not present in this subset.

[0105] An inconsistency of the set \mathcal{R} (which defines the operation of target computer system) is detected when a witness trace is found for at least one subset and no contradicting timed requirement is found for said witness trace. The testing method 50 may stop once at least one inconsistency is detected for at least one subset, or it may continue until e.g. all the subsets are processed.

[0106] In some embodiments, and as illustrated in figure 5, the testing method 50 may also comprise a step S55 of evaluating whether a stop criterion is satisfied. For instance, it is possible to consider a higher bound K_{TRmax} ($K_{TRmax} < N_{TR}$) on the size of the subset, such that the stop criterion is satisfied when the size of the subset reaches K_{TRmax} . Other stop conditions, or a combination thereof, may be considered in the stop criterion. In figure 5, the step S55 of evaluating the stop criterion is executed after step S52 of searching for a witness trace. If the stop criterion is not satisfied (reference S55b in figure 5), then the testing method 50 proceeds with step S53. In turn, if the stop criterion is satisfied (reference S55a in figure 5), then the iterations stop for this subset. However, other positions are possible, and the testing method 50 may also include more than one step S55 of evaluating a stop criterion, evaluating different respective stop conditions.

[0107] In the sequel, we describe specific preferred embodiments of testing method 50. It is emphasized that the present invention is not limited to the below exemplary embodiments. Variants of these embodiments are also within the scope of the present invention.

B.1. Embodiment 1: Incremental RT-Consistency Checking

[0108] In this subsection, we present an embodiment of the testing method 50 for rt-consistency checking. This embodiment uses computation tree logic, CTL, model checking.

[0109] Consider a set $\mathcal{R} = \{R_i\}_{i \in I}$ given as timed automata. As discussed above, in the product of R_i , the set of error states is the set of states in which at least one timed automaton is in its error state. Thus, given trace $\sigma \in \Sigma^*$, the predicate err is false in the state reached by reading σ in $\otimes \mathcal{R}$ if, and only if, $\sigma \models \mathcal{R}$. Rt-inconsistency checking then reduces to checking whether a finite trace satisfying $\neg \text{err}$ can be found such that err is inevitable from the last state.

Accordingly, the conflict detection criterion can be expressed by the following CTL formula to be checked in $\otimes \mathcal{R}$:

$$\mathbf{E}[\neg \text{err} \mathbf{U}(\neg \text{err} \wedge \mathbf{A} \text{Ferr})]$$

[0110] We do not formally define CTL and its semantics but only explain this specific formula. This formula says that there exists a finite execution ρ in which $\neg \text{err}$ holds (that is, $\forall 0 \leq i \leq |\rho|, \rho_i \models \neg \text{err}$), such that all continuations from state $\rho_{|\rho|-1}$ eventually lead to err , that is, err is inevitable from the last state of ρ .

[0111] If the formula is true, then there exists a finite trace σ such that $\sigma \models \mathcal{R}$ (since err is false), but for all infinite traces $\sigma' \in \Sigma^\infty$, $\sigma \sigma'$ sets err to true eventually. In other terms, $\sigma \sigma' \not\models \mathcal{R}$ for all $\sigma' \in \Sigma^\infty$, or equivalently $\sigma' \not\models \mathcal{R}$, witnessing that \mathcal{R} is rt-inconsistent. Such a finite trace σ , if it exists, corresponds to a witness trace for the rt-inconsistency of the

set \mathcal{R} , searched for in step S52 of the testing method 50. Basically, the witness trace σ satisfies all the timed requirements of the subset, but the witness trace has no extension that satisfies all said timed requirements of the subset.

[0112] The advantage of defining the conflict detection criterion using a CTL formula is that there exist efficient model checking tools (such as NuSMV) for checking if a finite state target computer system satisfies such a CTL formula. Additionally, if the formula holds, such model checking tools will also return a run witnessing this CTL formula, i.e. the witness trace searched for in step S52 of the testing method 50.

[0113] It should be noted that this CTL formula, together with model checking tools, can be used directly on the whole set \mathcal{R} , without considering subsets as discussed in reference to figure 5, if the size of the set \mathcal{R} is not too large.

[0114] When the size of the set \mathcal{R} is too large, then we can consider subsets of the set \mathcal{R} , as discussed above in reference to figure 5. The resulting check can only provide partial guarantee on rt-inconsistency. Indeed if $\mathcal{R}' \subseteq \mathcal{R}$, consistency of \mathcal{R} does not imply consistency of \mathcal{R}' , nor the opposite. Nevertheless, rt-inconsistencies can be detected, as shown in the next lemma.

Lemma 1:

[0115] Let $\sigma \in \Sigma^*$ be a witness trace for the rt-inconsistency of $\mathcal{R}' \subseteq \mathcal{R}$. If

$$\sigma \models \mathcal{R},$$

then σ is also a witness trace for the rt-inconsistency of \mathcal{R} .

Proof:

[0116] In fact, if σ is a witness trace of rt-inconsistency in \mathcal{R}' i.e.

$$\sigma \models \mathcal{R}'$$

but

$$\sigma \not\models \mathcal{R}',$$

it also implies

$$\sigma \not\models \mathcal{R}.$$

If additionally

$$\sigma \models \mathcal{R},$$

then we can conclude that σ is a witness trace of rt-inconsistency of \mathcal{R} .

[0117] Given the set \mathcal{R} and a bound

$$K_{TRmax} \leq |\mathcal{R}|,$$

we consider subsets \mathcal{R}' of the set \mathcal{R} of size up to K_{TRmax} , starting for instance with subsets of size 2 ($K_{TR} = 2$). If a subset

$$\mathcal{R}' \subseteq \mathcal{R}$$

is found to be rt-inconsistent with a witness trace σ , then we check whether

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$$\sigma \models \mathcal{R} \setminus \mathcal{R}'.$$

If this is the case (which implies that no contradicting timed requirement can be found), we can conclude using Lemma 1 that the set \mathcal{R} is rt-inconsistent and that σ is a witness trace of this rt-inconsistency.

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[0118] Otherwise (if $\sigma \not\models \mathcal{R} \setminus \mathcal{R}'$), we select a contradicting timed requirement $R \in \mathcal{R} \setminus \mathcal{R}'$ such that $\sigma \not\models R$, and

restart the analysis with $\mathcal{R}' \cup \{R\}$ (i.e. the subset \mathcal{R}' is augmented with the contradicting timed requirement R). It

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should be noted that if $\mathcal{R}' \cup \{R\}$ is inconsistent, then the trace σ identified during the previous iteration cannot be a

witness trace for $\mathcal{R}' \cup \{R\}$ since it violates R . This ensures that a new timed requirement will be added to the subset at each iteration. If no confirmed witness traces are found (i.e. either no further witness trace is found or there always exists a contradicting timed requirement), then the iterations may stop, and no rt-inconsistency is found for this subset.

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If $K_{TRmax} = |\mathcal{R}|$, then one can conclude that the set \mathcal{R} is rt-consistent. Otherwise ($K_{TRmax} < |\mathcal{R}|$), the check is only partial. In order to increase the precision (to have a better chance to detect rt-inconsistencies), one can increase the bound K_{TRmax} . In order to reduce the number of cases to check, thus giving up on completeness, one might restrict

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only to some subsets, for instance making sure that each timed requirement is covered by at least one subset.

[0119] An exemplary pseudo-code implementation for the present embodiment is given below, in the case where K_{TR}

= 2. In the example below, we write $\mathcal{R}' \models \mathbf{E}[\neg \text{err} \mathbf{U}(\neg \text{err} \wedge \text{AFerr})]$ for a subset \mathcal{R}' of timed automata and the

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CTL formula to mean that the product of these timed automata satisfies the CTL formula (i.e. there exists a witness trace satisfying the conflict detection criterion). It should be noted that, in order to avoid checking the same subsets of the set \mathcal{R} several times (e.g. when augmenting the subsets), one can store the subsets seen so far and break the while loop when a subset \mathcal{R}' has already been processed.

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```

for all pairs  $\{R_1, R_2\} \subseteq \mathcal{R}$  do
     $\mathcal{R}' \leftarrow \{R_1, R_2\}$  // subset
    while  $|\mathcal{R}'| \leq K_{TRmax}$  and  $\mathcal{R}' \models \mathbf{E}[\neg \text{err} \mathbf{U}(\neg \text{err} \wedge \text{AFerr})]$  do
         $\sigma \leftarrow$  witness trace for  $\mathbf{E}[\neg \text{err} \mathbf{U}(\neg \text{err} \wedge \text{AFerr})]$ 
        if  $\exists R \in \mathcal{R}, \sigma \not\models R$  then
             $\mathcal{R}' \leftarrow \mathcal{R}' \cup R$  // subset is augmented
        else
            return  $\sigma$  // rt-inconsistency of  $\mathcal{R}$  detected
        end
    end
end
return  $\emptyset$  // no witness trace found
    
```

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B.2. Embodiment 2: Incremental Partial-Consistency Checking

[0120] In this subsection, we present an embodiment of the testing method 50 for partial consistency checking. This embodiment uses timed requirements modeled as a Simplified Universal Patterns, SUPs.

[0121] Ideally, we would like to check Equation (2) for all pairs $\{R_1, R_2\}$ of requirements with respect to

$\mathcal{R}_{inv} = \mathcal{R} \setminus \{R_1, R_2\}$. In fact, considering such a subset \mathcal{R}_{inv} makes sure that traces do not trivially violate timed requirements. This is costly in general, and the present embodiment starts with an empty \mathcal{R}_{inv} and let it grow incrementally by adding timed requirements as needed. Hence, in the present embodiment, the subset (as defined in reference to figure 5) corresponds to $\mathcal{R}_{inv} \cup \{R_1, R_2\}$ (which is initially equal to $\{R_1, R_2\}$ since \mathcal{R}_{inv} is initially empty, such that $K_{TR} = 2$). For the present embodiment, the conflict detection criterion is satisfied when Equation (2) above fails and searching for witness traces may use e.g. a Satisfiability Modulo Theories, SMT, solver.

[0122] Given the set \mathcal{R} , a pair $\{R_1, R_2\}$, a subset $\mathcal{R}_{inv} \subseteq \mathcal{R} \setminus \{R_1, R_2\}$, integer parameters $\alpha, \beta > 0$, the (α, β) -bounded partial-consistency checking consists in verifying Equation (2). If the check is negative (i.e. Equation (2) fails), then the conflict detection criterion is satisfied and there exists $k < \alpha$ and a pair of traces σ_1, σ_2 satisfying the left part of Equation (2) (σ_i is a witness trace for the case $i \in \{1, 2\}$), while the right part of Equation (2) is not satisfiable. Hence, in the present case, there are two witness traces σ_1, σ_2 found during step S52 of the testing method 50. We check whether

$\sigma_i \models \mathcal{R}$ for some $i \in \{1, 2\}$ in which case the witness trace is returned, and a partial inconsistency is detected. Otherwise, we select a timed requirement $R' \in \mathcal{R} \setminus \{\mathcal{R}_{inv} \cup \{R_1, R_2\}\}$ such that $\sigma_i \not\models R'$, and we add it to \mathcal{R}_{inv} (i.e. the subset $\mathcal{R}_{inv} \cup \{R_1, R_2\}$ is augmented with the contradicting timed requirement R'), and we restart the analysis with $\{R_1, R_2\}$ and \mathcal{R}_{inv} . Subsequent iterations may discard the witness trace σ_i and look for other traces.

[0123] An exemplary pseudo-code implementation for the present embodiment is given below.

```

for all pairs  $\{R_1, R_2\} \subseteq \mathcal{R}$  do
     $\mathcal{R}_{inv} \leftarrow \emptyset$  // subset corresponds to  $\mathcal{R}_{inv} \cup \{R_1, R_2\}$ 
    while Equation (2) fails do
         $(\sigma_1, \sigma_2) \leftarrow$  witness trace for the premise of (2) for some  $k \leq \alpha$ 
        if  $\exists i \in \{1, 2\}, \sigma_i \models \mathcal{R}$  then
            return  $\sigma_i$  // rt-inconsistency of  $\mathcal{R}$  detected
  
```

```

5      else
        if  $\mathcal{R} = \mathcal{R}_{inv} \cup \{R_1, R_2\}$  then
          break
        else
10         Choose  $R' \in \mathcal{R}$  such that  $\sigma_i \not\approx R'$  for some  $i \in \{1,2\}$ 
            $\mathcal{R}_{inv} \leftarrow \mathcal{R}_{inv} \cup R'$  // subset  $\mathcal{R}_{inv} \cup \{R_1, R_2\}$  is augmented
        end
15      end
    end
20  end
  return  $\emptyset$  // no witness trace found

```

[0124] Before proving the absence of false positives as output to the present embodiment, we prove an intermediate lemma relating to some witness of bounded-partial inconsistency and rt-inconsistency.

Lemma 2:

[0125] Let σ_1, σ_2 and k be witnesses of bounded partial inconsistency for $\mathcal{R}_{inv} \subseteq \mathcal{R}$, i.e. counterexamples of Equation (2). For any i , if additionally $\sigma_i \models \mathcal{R}$, then σ_i is also a witness trace for the rt-inconsistency of \mathcal{R} .

Proof:

[0126] Suppose $\sigma_1, \sigma_2 \in \Sigma^\alpha$ and $k \leq \alpha$ witness (α, β) -bounded partial inconsistency, thus $\forall i \in \{1,2\}, \sigma_i \in \Sigma^\alpha, \Sigma^\alpha$, $\sigma_i \models \mathcal{R}_{inv} \cup \{R_i\} \wedge \mathbf{Trig}_{(R_1, R_2), k}(\sigma_i)$ but there is no $\sigma \in \Sigma^{\alpha+\beta}$ such that $\sigma \models \mathcal{R}_{inv} \cup \{R_1, R_2\} \wedge \mathbf{Trig}_{(R_1, R_2), k}(\sigma)$. Suppose by contradiction that σ_i is not a witness trace of rt-inconsistency, i.e. $\sigma_i \not\approx \mathcal{R}$. By definition, there is an infinite continuation $\sigma' \in \Sigma^\infty$ such that $\sigma\sigma' \models \mathcal{R}$. Let p be the prefix of $\sigma\sigma'$ of length $\alpha + \beta$. Since σ is a prefix of p and $\mathbf{Trig}_{(R_1, R_2), k}(\sigma)$, we also get $\mathbf{Trig}_{(R_1, R_2), k}(p)$. Now the right part of Equation (2) is false, while $p \in \Sigma^{\alpha+\beta}$ and $p \models \mathcal{R} \supseteq \mathcal{R}_{inv} \cup \{R_1, R_2\}$ and this is a contradiction, such that we can conclude that $\sigma_i \not\approx \mathcal{R}$.

[0127] The following lemma then shows that witness traces returned by the present embodiment are witnesses to rt-inconsistency.

Lemma 3:

[0128] Let \mathcal{R} be a set of SUP requirements, and σ be a finite trace returned by the present embodiment (at line 6 of the pseudo-code), then σ is a witness trace for rt-inconsistency for \mathcal{R} .

Proof:

[0129] Assume that the present embodiment returned a witness trace $\sigma \in \Sigma^*$ for the outer iteration with $R_1, R_2 \in \mathcal{R}$, and inner iteration $\mathcal{R}_{inv} \subseteq \mathcal{R}$. The present embodiment ensures that $\sigma \models \mathcal{R}$ (line 5 of the pseudo-code above). We then use Lemma 2 to conclude that σ is a witness trace for rt-inconsistency for the set \mathcal{R} .

B.3. Embodiment 3: Partial RT-Consistency Checking

[0130] In this subsection, we present an embodiment of the testing method 50 for partial consistency checking. Basically, we divide the rt-consistency checking problem into several smaller problems. Intuitively, for all (or part of) subsets of a given set \mathcal{R} of requirements, we check an absence of conflict involving all the timed requirements in the subset. Notice that a divided problem for a large subset (possibly the original set itself) would be still simpler than the original problem, since each divided problem considers only the case where all the timed requirements in the subset conflict simultaneously.

[0131] For a timed requirement represented by a deterministic timed automaton

$$\mathcal{T} = \langle S, S_0, AP, \mathcal{X}, T, E \rangle$$

and a trace $\sigma \in \Sigma^*$, let $(s_i, v_i)_{i \in [0, |\sigma|]}$ denote a corresponding run. Then, for a transition $t \in T$ and a natural number $k \in \mathbb{N}$, we define:

$$\mathbf{ErrAt}(\mathcal{T}, t, \sigma, k) = (\mathbf{tgt}(t) \in E) \wedge (\mathbf{src}(t) = s_k) \wedge (v_k \models \mathbf{guard}(t))$$

[0132] Intuitively, $\mathbf{ErrAt}(\mathcal{T}, t, \sigma, k)$ holds if transition t , leading to an error state, is available after step k along a trace σ (for some valuation of the atomic propositions).

[0133] For a set of timed automata

$$\mathcal{R} = \{\mathcal{T}_i = \langle S_i, S_{i,0}, AP_i, \mathcal{X}_i, T_i, E_i \rangle\}_{i \in \mathcal{J}}$$

that represents a set of timed requirements, we define: **conflict**

$$(\mathcal{R}, \sigma, k) = \exists (t_i)_{i \in \mathcal{J}} \in (\times T_i)_{i \in \mathcal{J}}, \left(\models \bigvee_i^{\mathcal{J}} \mathbf{bool}_i(t_i) \right)$$

$$\wedge \forall \mathcal{J}' \subset \mathcal{J}, \mathcal{J}' \neq \emptyset \rightarrow \neg \left(\models \bigvee_i^{\mathcal{J}'} \mathbf{bool}_i(t_i) \right)$$

$$\wedge \forall \mathcal{T}_i \in \mathcal{R}, \quad \mathbf{ErrAt}(\mathcal{T}_i, t_i, \sigma, k)$$

[0134] Intuitively, $\mathbf{conflict}(\mathcal{R}, \sigma, k)$ holds if for all $\sigma' \in \Sigma$, $\sigma_{\dots k} \sigma'$ takes some transition into an error state at step $k + 1$. In other words, after an execution represented by $\sigma_{\dots k}$ all the timed requirements in \mathcal{R} conflict each other, and any execution at step $k + 1$ will violate at least one of the time requirements in \mathcal{R} . Moreover, in $\mathbf{conflict}(\mathcal{R}, \sigma, k)$, we only consider conflicts among all of the timed requirements in \mathcal{R} , by specifying the condition

$$\neg \left(\bigvee_i^{j_i} \mathbf{bool}_i(t_i) \right).$$

5 That enables incremental checking of conflict for each subset of \mathcal{R} .

[0135] Now we define a notion of partial rt-consistency. For a set \mathcal{R} of timed automata and a subset $\mathcal{R}' \subseteq \mathcal{R}$, \mathcal{R}' is α -bounded partially rt-inconsistent with $\mathcal{R}_{inv} \subseteq \mathcal{R} \setminus \mathcal{R}'$ if, and only if

$$\exists k \leq \alpha, \exists \sigma \in \Sigma^k, \sigma \models \{\mathcal{R}' \cup \mathcal{R}_{inv}\} \wedge \mathbf{conflict}(\mathcal{R}', \sigma, k) \quad (3)$$

15 **[0136]** Furthermore, for $K_{TRmax} \in [1, |\mathcal{R}|]$, we define K_{TRmax} -partial rt-consistency as follows:

$$\forall \mathcal{R}' \subseteq \mathcal{R}, |\mathcal{R}'| \leq K_{TRmax} \rightarrow \left(\forall k \leq \alpha, \forall \sigma \in \Sigma^k, \sigma \models \mathcal{R} \rightarrow \neg \mathbf{conflict}(\mathcal{R}', \sigma, k) \right)$$

20 **[0137]** K_{TRmax} -partial rt-consistency ensures that there is no conflict among a subset of \mathcal{R} of size 1 to K_{TRmax} up to step α . Thus, $|\mathcal{R}|$ -partial rt-consistency implies rt-consistency up to step α , and $|\mathcal{R}|$ -partial rt-inconsistency implies rt-inconsistency.

25 **[0138]** In the present embodiment, the conflict detection criterion is satisfied when Equation (3) above fails and searching for witness traces may use e.g. a SMT solver. Basically, the witness trace σ is such that it satisfies all the timed requirements of the subset and, for all extensions of the witness trace, at least one timed requirement of the subset will always be violated at next step.

30 **[0139]** The subset (as defined in reference to figure 5) corresponds to $\mathcal{R}_{inv} \cup \mathcal{R}'$ (which is initially equal to \mathcal{R}' since \mathcal{R}_{inv} is initially empty).

35 **[0140]** An exemplary pseudo-code implementation for the present embodiment is given below. A witness trace σ returned by the present embodiment is clearly a witness of rt-inconsistencies of bounded size for a subset \mathcal{R}' . Since the present embodiment ensures that σ satisfies \mathcal{R} (line 5 in the pseudo-code below), again using Lemma 1 we can conclude that σ is a witness trace of rt-inconsistency.

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```

for all  $\mathcal{R}' \subseteq \mathcal{R}$  such that  $|\mathcal{R}'| \leq K_{TR}$  do
     $\mathcal{R}_{inv} \leftarrow \emptyset$  // subset corresponds to  $\mathcal{R}_{inv} \cup \mathcal{R}'$ 
    while Equation (3) fails do
         $(k, \sigma) \leftarrow k \leq \alpha$  and  $\sigma$  witness trace for Equation (3)
        if  $\sigma \models \mathcal{R}$  then
            return  $\sigma$  // inconsistency detected
        else
            if  $\mathcal{R} = \mathcal{R}_{inv} \cup \mathcal{R}'$  then
                break
            else
                Choose  $R \in \mathcal{R}$  such that  $\sigma \not\models R$ 
                 $\mathcal{R}_{inv} \leftarrow \mathcal{R}_{inv} \cup R$  // subset  $\mathcal{R}_{inv} \cup \mathcal{R}'$  is augmented
            end
        end
    end
end
return  $\emptyset$  // no witness trace found

```

[0141] To illustrate how partial rt-consistency checking works, consider the following SUP requirements:

- $R_1: (p, \text{true}, \text{true})[0; 0] \xrightarrow{[1; 1]} (q, \text{true}, r)[1; 1]$;
- $R_2: (p, \text{true}, \text{true})[0; 0] \xrightarrow{[2; 2]} (\neg q, \text{true}, \text{true})[0; 0]$;
- $R_3: (p, \text{true}, \text{true})[0; 0] \xrightarrow{[4; 4]} (r, \text{true}, \text{true})[0; 0]$.

[0142] Let \mathcal{T}_1 , \mathcal{T}_2 and \mathcal{T}_3 denote timed automata representing R_1 , R_2 and R_3 respectively. For each timed automaton, there exists the following transitions into error state:

- $\mathcal{T}_1: (\text{delay}_1, c_1 = 1, \neg q, \text{err}_1), (\text{act}_1, c_1 = 1, \neg r, \text{err}_1)$;
- $\mathcal{T}_2: (\text{delay}_2, c_2 = 2, q, \text{err}_2)$;
- $\mathcal{T}_3: (\text{delay}_3, c_3 = 4, \neg r, \text{err}_3)$.

[0143] A tuple $t = (s, \text{clock}, \text{bool}, s')$ denotes a transition from state s to state s' with time condition clock and Boolean condition bool . In other words, $\text{src}(t) = s$, $\text{tgt}(t) = s'$, $\text{guard}(t) = \text{clock}$ and $\text{bool}(t) = \text{bool}$. For example, $(\text{delay}_1, c_1 = 1, \neg q,$

$\text{err}_1)$ denotes that if \mathcal{T}_1 is in the state delay_1 and the value of its clock is 1 at some step i , and the condition q does not hold at step $i + 1$ then, \mathcal{T}_1 transitions to the error state err_1 at step $i + 1$.

[0144] In 2-partial rt-consistency checking, the subsets $\{R_1, R_2\}$, $\{R_2, R_3\}$, $\{R_1, R_3\}$ are checked. Let t_1^1 , t_2^1 , t_1^2 , t_1^3 denote $(\text{delay}_1, c_1 = 1, \neg q, \text{err}_1)$, $(\text{act}_1, c_1 = 1, \neg r, \text{err}_1)$, $(\text{delay}_2, c_2 = 2, q, \text{err}_2)$, $(\text{delay}_3, c_3 = 4, \neg r, \text{err}_3)$ respectively. For the subset $\{R_1, R_2\}$, there exists a pair of transitions (t_1^1, t_2^1) which satisfies the condition $|\neq \mathbf{bool}(t_1^1) \vee \mathbf{bool}(t_2^1)$ since $|\neq \neg q \vee q$ holds. Thus, if there exists a trace σ and $k \in \mathbb{N}$ that satisfies

$$\mathbf{ErrAt}(\mathcal{T}_1, t_1^1, \sigma, k) \wedge \mathbf{ErrAt}(\mathcal{T}_2, t_1^2, \sigma, k)$$

then, $\mathbf{conflict}(\{R_1, R_2\}, \sigma, k)$ holds. In that case, if σ satisfies R_1 , R_2 and R_3 until step k , the timed requirements are rt-inconsistent.

[0145] However, by definition of \mathbf{ErrAt} , \mathcal{T}_1 must be in the state delay_1 and $c_1 = 1$, and \mathcal{T}_2 must be in the state delay_2 and $c_2 = 2$, at step k . It will never occur since

$$\mathcal{T}_1$$

and

$$\mathcal{T}_2$$

transition to delay_1 (resp. delay_2) simultaneously and the value of their clock are always same. In practice, that may be automatically checked by an SMT solver until step α . Thus, R_1 and R_2 are partially rt-consistent. For $\{R_2, R_3\}$ and $\{R_1, R_3\}$, there is no pair (t, t') that satisfies $|\neq \mathbf{bool}(t) \vee \mathbf{bool}(t')$. Therefore, the timed requirements R_1 , R_2 and R_3 are 2-partially rt-consistent.

[0146] In 3-partial rt-consistency checking, the original set $\{R_1, R_2, R_3\}$ is checked. In this case, there exists a triplet of transitions (t_1^1, t_1^2, t_1^3) that satisfies $|\neq \mathbf{bool}(t_1^1) \vee \mathbf{bool}(t_1^2) \vee \mathbf{bool}(t_1^3)$. However, the triplet does not satisfy the condition $|\neq \mathbf{bool}(t_1^1) \vee \mathbf{bool}(t_1^2)$ since $|\neq \neg q \vee q$ holds. Then, we can conclude that t_1^1 , t_1^2 and t_1^3 do not become available at the same time, since it is already confirmed in 2-partial rt-consistency checking that there is no trace σ and $k \in \mathbb{N}$ that satisfies

$$|\neq \mathbf{ErrAt}(\mathcal{T}_1, t_1^1, \sigma, k) \wedge \mathbf{ErrAt}(\mathcal{T}_2, t_1^2, \sigma, k).$$

Thus obviously, there is no σ and k that satisfies

$$\mathbf{ErrAt}(\mathcal{T}_1, t_1^1, \sigma, k) \wedge \mathbf{ErrAt}(\mathcal{T}_2, t_1^2, \sigma, k) \wedge \mathbf{ErrAt}(\mathcal{T}_3, t_1^3, \sigma, k).$$

Therefore, R_1 , R_2 and R_3 are 3-partially rt-consistent. As a result, the timed requirements R_1 , R_2 and R_3 are rt-consistent.

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Claims

1. A computer implemented method (50) for testing the operation of a target computer system, said operation of the target computer system being constrained by a set of timed requirements, wherein a timed requirement corresponds to constraints, including timing constraints, between outputs of the target computer system and inputs to said target computer system, wherein said method is **characterized in that** it comprises (S51) defining subsets of timed requirements of the set, and for each subset of a plurality of subsets, iterating steps of:

- (S52) searching for an execution sequence of the target computer system, referred to as witness trace, satisfying a criterion for detecting a conflict between timed requirements of the subset;
- when a witness trace is found: (S53) searching for a contradicting timed requirement among the timed requirements of the set which are not present in the subset, which prevents the witness trace to occur when considered with the subset;
- when a contradicting timed requirement is found: (S54) augmenting the subset by including the contradicting timed requirement in the subset;

wherein an inconsistency of the operation of the target computer system constrained by the set of timed requirements is detected when a witness trace is found for at least one subset and no contradicting timed requirement is found for said witness trace.

2. Method (50) according to claim 1, wherein the subset is augmented with contradicting timed requirements only up to a predetermined number of timed requirements.

3. Method (50) according to claim 1 or 2, wherein the criterion for detecting a conflict is satisfied when a real-time-inconsistency is detected or when a partial inconsistency is detected.

4. Method (50) according to any one of the preceding claims, wherein, the conflict detection criterion is expressed using Computation Tree Logic, CTL, and searching for a witness trace uses a model checking tool.

5. Method (50) according to any one of claims 1 to 3, wherein, searching for a witness trace uses a Satisfiability Modulo Theories, SMT, solver.

6. Method (50) according to any one of the preceding claims, wherein each timed requirement is modeled as a timed automaton.

7. Method (50) according to any one of the preceding claims, wherein each timed requirement is modeled as a Simplified Universal Pattern, SUP.

8. Computer program product comprising instructions which, when executed by at least one processor, configure said at least one processor to carry out a testing method (50) according to any one of the preceding claims.

9. Computer-readable storage medium comprising instructions which, when executed by at least one processor, configure said at least one processor to carry out a testing method (50) according to any one of claims 1 to 7.

10. Testing computer system for testing the operation of a target computer system, said testing computer system comprising at least one processor configured to carry out a testing method (50) according to any one of claims 1 to 7.

Patentansprüche

1. Computerimplementiertes Verfahren (50) zum Testen des Betriebs eines Zielcomputersystems, wobei der Betrieb des Zielcomputersystems durch einen Satz zeitlich festgelegter Anforderungen beschränkt ist, wobei eine zeitlich festgelegte Anforderung Beschränkungen, einschließlich zeitlicher Beschränkungen, zwischen Ausgaben des Zielcomputersystems und Eingaben in das Zielcomputersystem entspricht, wobei das Verfahren **dadurch gekennzeichnet ist, dass** es (S51) ein Definieren von Teilsätzen zeitlich festgelegter Anforderungen des Satzes und für jeden Teilsatz einer Mehrzahl von Teilsätzen ein Iterieren der folgenden Schritte umfasst:

- (S52) Suchen nach einer Ausführungssequenz des Zielcomputersystems, als eine Zeugenspur bezeichnet, welche ein Kriterium zum Detektieren eines Konflikts zwischen zeitlich festgelegten Anforderungen des Teilsatzes erfüllt;
- wenn eine Zeugenspur gefunden worden ist: (S53) Suchen nach einer widersprechenden zeitlich festgelegten Anforderung unter den zeitlich festgelegten Anforderungen des Satzes, welche nicht in dem Teilsatz vorhanden sind, welche verhindert, dass die Zeugenspur auftritt, wenn mit dem Teilsatz betrachtet;
- wenn eine widersprechende zeitlich festgelegte Anforderung gefunden worden ist: (S54) Erweitern des Teilsatzes durch Aufnehmen der widersprechenden zeitlich festgelegten Anforderung in den Teilsatz;

wobei eine Inkonsistenz des Zielcomputersystems, welches durch den Satz zeitlich festgelegter Anforderungen beschränkt ist, detektiert wird, wenn für wenigstens einen Teilsatz eine Zeugenspur gefunden wird und keine widersprechende zeitlich festgelegte Anforderung für die Zeugenspur gefunden wird.

2. Verfahren (50) nach Anspruch 1, wobei der Teilsatz nur bis zu einer vorbestimmten Anzahl zeitlich festgelegter Anforderungen mit widersprechenden zeitlich festgelegten Anforderungen erweitert wird.

3. Verfahren (50) nach Anspruch 1 oder 2, wobei das Kriterium zum Detektieren eines Konflikts erfüllt ist, wenn eine Echtzeit-Inkonsistenz detektiert wird oder wenn eine Teil-Inkonsistenz detektiert wird.

4. Verfahren (50) nach einem der vorhergehenden Ansprüche, wobei das Konfliktdetektionskriterium unter Verwendung von Computation Tree Logic, CTL, ausgedrückt wird und das Suchen nach einer Zeugenspur ein Model-Checking-Tool verwendet.

5. Verfahren (50) nach einem der Ansprüche 1 bis 3, wobei das Suchen nach einer Zeugenspur einen Satisfiability-Modulo-Theories, SMT, -Solver verwendet.

6. Verfahren (50) nach einem der vorhergehenden Ansprüche, wobei jede zeitlich festgelegte Anforderung als ein zeitlich festgelegter Automat modelliert wird.

7. Verfahren (50) nach einem der vorhergehenden Ansprüche, wobei jede zeitliche Anforderung als ein Simplified-Universal-Pattern, SUP, modelliert wird.

8. Computerprogrammprodukt, umfassend Anweisungen, welche, wenn durch wenigstens einen Prozessor ausgeführt, den wenigstens einen Prozessor derart konfigurieren, dass er ein Testverfahren (50) nach einem der vorhergehenden Ansprüche durchführt.

9. Computerlesbares Speichermedium, umfassend Anweisungen, welche, wenn durch wenigstens einen Prozessor ausgeführt, den wenigstens einen Prozessor derart konfigurieren, dass er ein Testverfahren (50) nach einem der Ansprüche 1 bis 7 durchführt.

10. Testcomputersystem zum Testen des Betriebs eines Zielcomputersystems, wobei das Testcomputersystem wenigstens einen Prozessor umfasst, welcher dazu eingerichtet ist, ein Testverfahren (50) nach einem der Ansprüche 1 bis 7 durchzuführen.

Revendications

1. Procédé mis en œuvre par ordinateur (50) pour tester le fonctionnement d'un système informatique cible, ledit fonctionnement du système informatique cible étant contraint par un ensemble d'exigences temporelles, dans lequel

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une exigence temporelle correspond à des contraintes, incluant des contraintes de timing, entre des sorties du système informatique cible et des entrées dudit système informatique cible, dans lequel ledit procédé est **caractérisé en ce qu'il** comprend (S51) une définition de sous-ensembles d'exigences temporelles de l'ensemble, et pour chaque sous-ensemble parmi une pluralité de sous-ensembles, l'itération des étapes suivantes :

5

- (S52) une recherche d'une séquence d'exécution du système informatique cible, dite trace témoin, vérifiant un critère de détection d'un conflit entre des exigences temporelles du sous-ensemble ;
- lorsqu'une trace témoin est trouvée : (S53) une recherche d'une exigence temporelle contradictoire parmi les exigences temporelles de l'ensemble qui ne sont pas présentes dans le sous-ensemble, qui empêche la trace témoin de survenir lorsqu'elle est prise en compte avec le sous-ensemble ;
- lorsqu'une exigence temporelle contradictoire est trouvée : (S54) une augmentation du sous-ensemble en incluant l'exigence temporelle contradictoire dans le sous-ensemble ;

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dans lequel une incohérence du fonctionnement du système informatique cible contraint par l'ensemble d'exigences temporelles est détectée lorsqu'une trace témoin est trouvée pour au moins un sous-ensemble et aucune exigence temporelle contradictoire n'est trouvée pour ladite trace témoin.

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2. Procédé (50) selon la revendication 1, dans lequel le sous-ensemble est augmenté avec des exigences temporelles contradictoires uniquement jusqu'à un nombre prédéterminé d'exigences temporelles.
- 20
3. Procédé (50) selon la revendication 1 ou 2, dans lequel le critère de détection d'un conflit est satisfait lorsqu'une incohérence de temps réel est détectée ou lorsqu'une incohérence partielle est détectée.
4. Procédé (50) selon l'une quelconque des revendications précédentes, dans lequel le critère de détection de conflit est exprimé en utilisant une logique d'arborescence de calcul, CTL, et la recherche d'une trace témoin utilise un outil de contrôle de modèle.
- 25
5. Procédé (50) selon l'une quelconque des revendications 1 à 3, dans lequel la recherche d'une trace témoin utilise un solveur de satisfiabilité modulo théories, SMT.
- 30
6. Procédé (50) selon l'une quelconque des revendications précédentes, dans lequel chaque exigence temporelle est modélisée en tant qu'automate temporel.
7. Procédé (50) selon l'une quelconque des revendications précédentes, dans lequel chaque exigence temporelle est modélisée en tant que motif universel simplifié, SUP.
- 35
8. Produit programme d'ordinateur comprenant des instructions qui, lorsqu'elles sont exécutées par au moins un processeur, configurent ledit au moins un processeur pour réaliser un procédé de test (50) selon l'une quelconque des revendications précédentes.
- 40
9. Support de stockage lisible par ordinateur comprenant des instructions qui, lorsqu'elles sont exécutées par au moins un processeur, configurent ledit au moins un processeur pour réaliser un procédé de test (50) selon l'une quelconque des revendications 1 à 7.
- 45
10. Système informatique de test pour tester le fonctionnement d'un système informatique cible, ledit système informatique de test comprenant au moins un processeur configuré pour réaliser un procédé de test (50) selon l'une quelconque des revendications 1 à 7.

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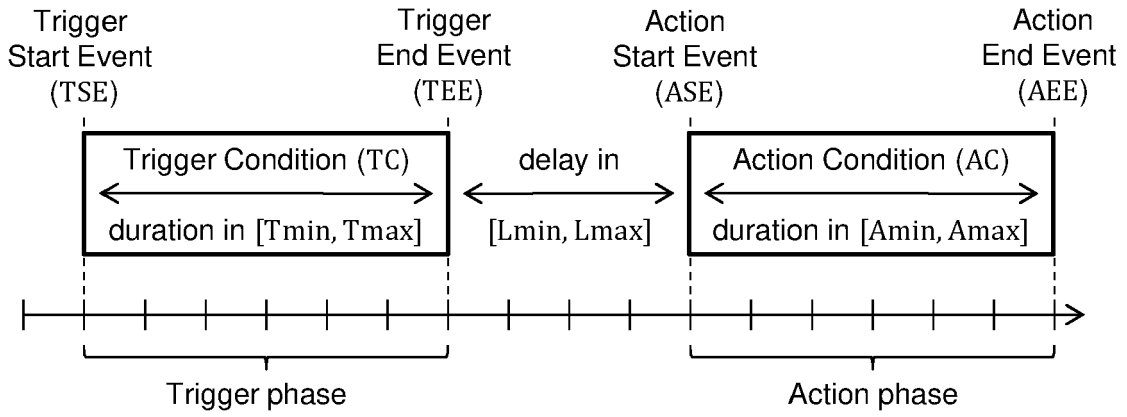


Fig. 1

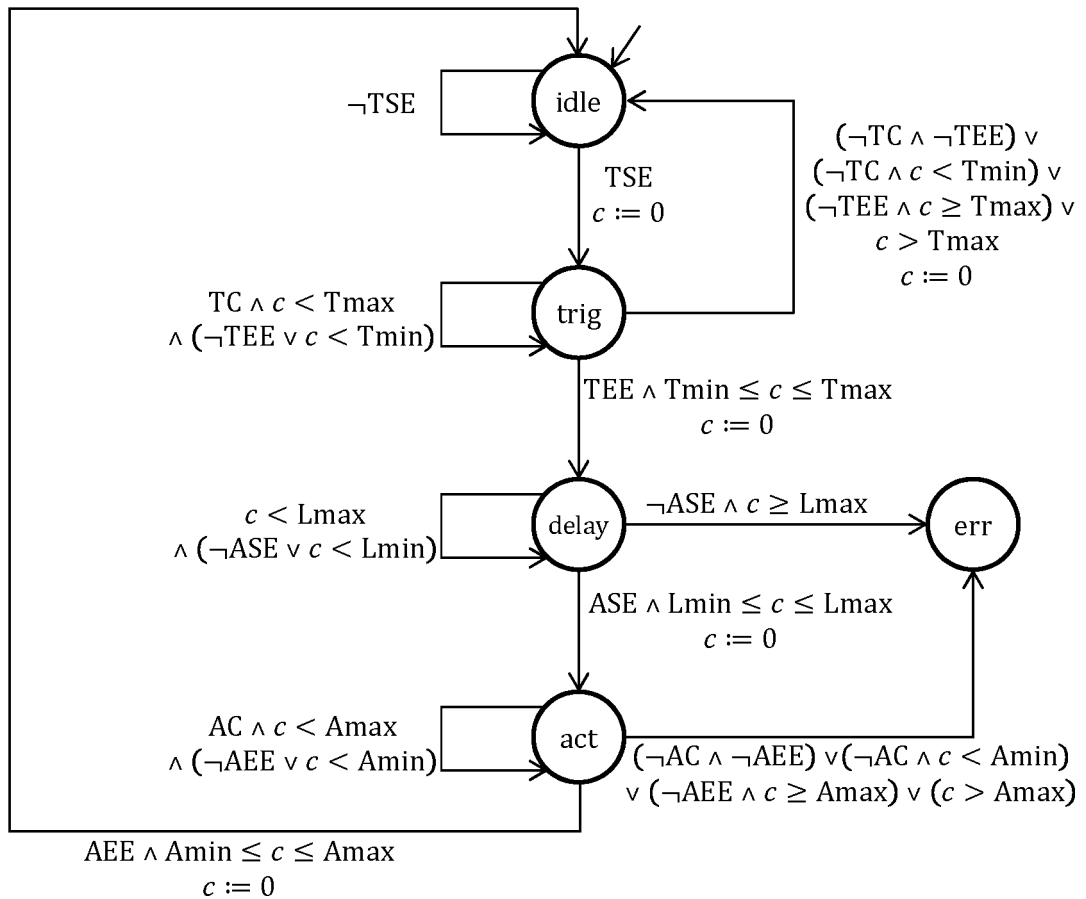


Fig. 2

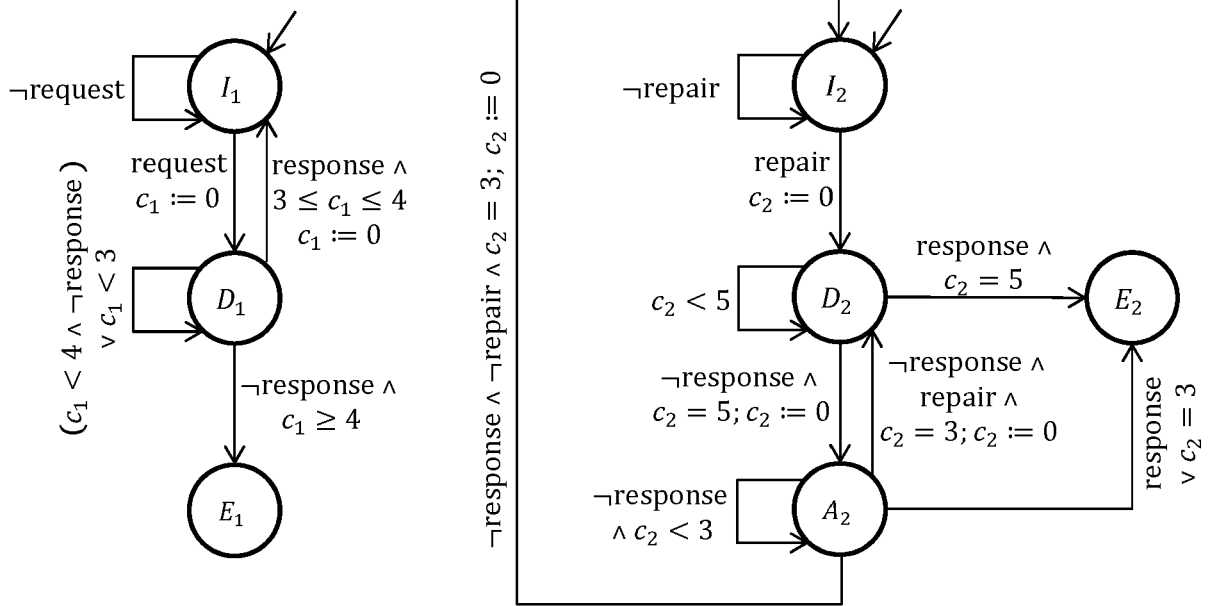


Fig. 3

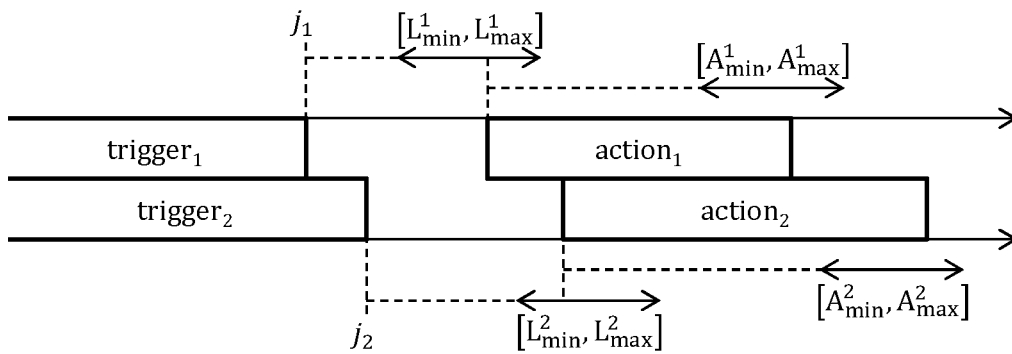


Fig. 4

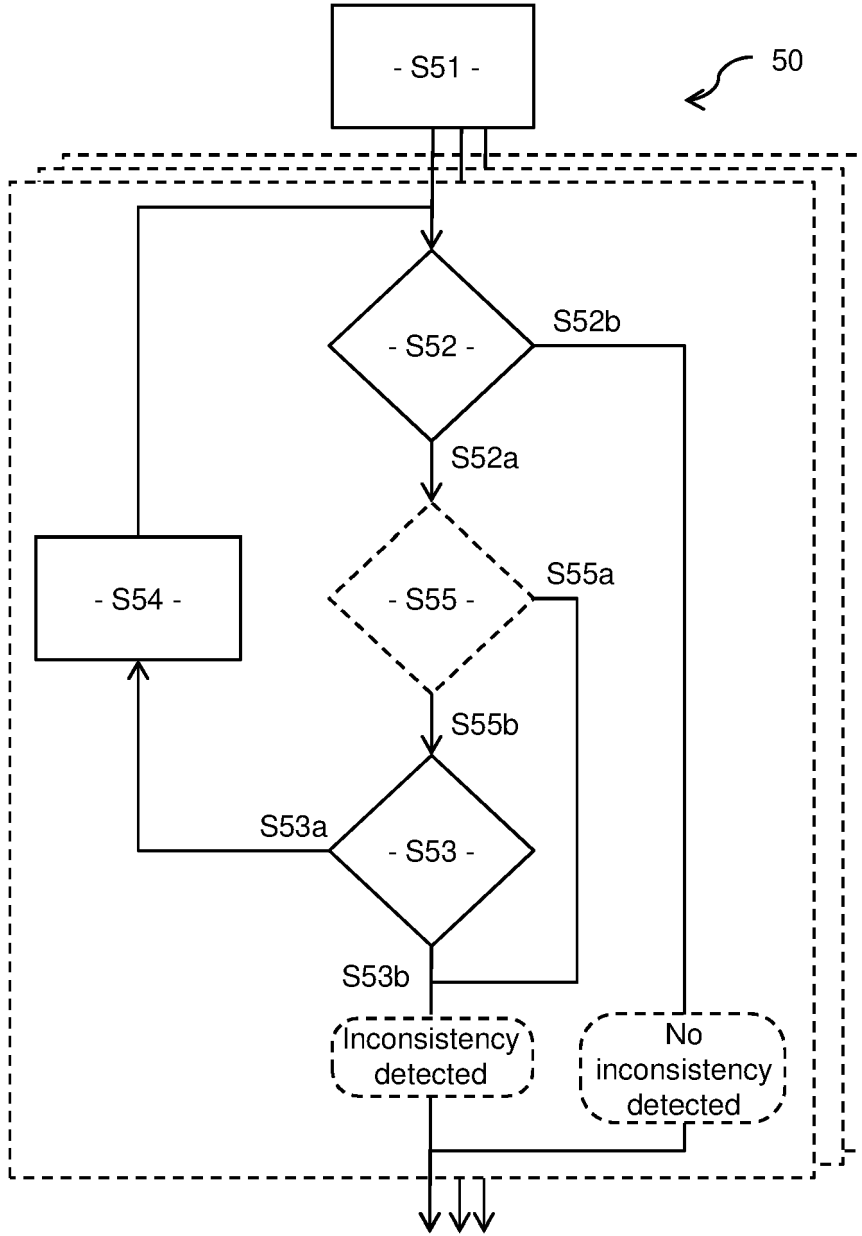


Fig. 5

REFERENCES CITED IN THE DESCRIPTION

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