

Corentin Ferry

Curriculum Vitae

Education

- Since September 2019 **Ph.D. in Computer Science**, *Université de Rennes 1 and Colorado State University*, Rennes, France and Fort Collins, CO, USA
Joint Ph.D curriculum (co-tutelle) between Université de Rennes 1 and Colorado State University. Supervision by Prof. Steven Derrien (UR1), Prof. Sanjay Rajopadhye (CSU); advised by Dr. Tomofumi Yuki (INRIA).
- 2016-2019 **Master's Degree in Computer Science**, *Université de Rennes 1*, Rennes, France
"Good" mention (15.027/20)
- 2015-2019 **ENS Rennes Magisterium**, *École normale supérieure de Rennes*, Rennes, France
Civil servant ("Fonctionnaire stagiaire") at ENS Rennes.
- 2016-2017 **Erasmus exchange**, *École polytechnique fédérale de Lausanne (EPFL)*, Lausanne, Switzerland
- 2015-2016 **Bachelor's Degree in Computer Science**, *Université de Rennes 1*, Rennes, France
"Good" mention (15.868/20)
- 2012-2015 **Preparatory classes "MPSI/MP**"**, *Lycée Thiers*, Marseille, France

Research experience

- Since September 2019 **Ph.D. student**, *IRISA*, Rennes, France
Ph.D on dynamic data compression techniques for FPGA accelerators. Direction: Prof. Steven Derrien (UR1), Prof. Sanjay Rajopadhye (CSU). Advisor (2019-2021): Dr. Tomofumi Yuki (INRIA).
- February - June 2019 **Research intern**, *Colorado State University*, Fort Collins, CO, USA
Advisors: Prof. Steven Derrien (UR1), Prof. Sanjay Rajopadhye (CSU) and Dr. Tomofumi Yuki (INRIA). Polyhedral compiler pass to detect data worth grouping for compression.
- September 2017 - June 2018 **Research intern**, *Colorado State University*, Fort Collins, CO, USA
Advisors: Prof. Sanjay Rajopadhye (CSU), Prof. Louis-Noël Pouchet (CSU). I/O complexity of programs; automated HLS + software to SoC pipeline.
- May - July 2016 **Research intern**, *IRISA*, Rennes, France
Supervised by Simon Rokicki (UR1) and Prof. Steven Derrien (UR1). Coarse-Grained Reconfigurable Architecture on FPGA with HLS + scheduler compiler pass.

Publications

- 2021 **Corentin Ferry**, Steven Derrien, Sanjay Rajopadhye, Tomofumi Yuki. *Canonical Facet Allocation: A Burst-Friendly Data Layout*. 2021 GdR SoC Workshop. Poster.
- 2018 Robin Scheibler, Juan Azcarreta, René Beuchat and **Corentin Ferry**. *Pyramic: Full Stack Open Microphone Array Architecture and Dataset*. 2018 16th International Workshop on Acoustic Signal Enhancement (IWAENC) (2018): 226-230. DOI: 10.1109/IWAENC.2018.8521337
- 2017 Eric Bezzam, Robin Scheibler, Juan Azcarreta, Hanjie Pan, Matthieu Simeoni, René Beuchat, Paul Hurley, Basile Bruneau, **Corentin Ferry**, and Sepand Kashani. *Hardware and software for reproducible research in audio array signal processing*. 2017 IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP) (2017): 6591-6592. DOI: 10.1109/ICASSP.2017.8005297

Teaching

- Fall 2021 **Teaching assistant**, *Colorado State University*, Fort Collins, CO, USA
CS 270 (Computer Organization). Instructor: Phil Sharp. 96 hours (recitations) + 64 hours (labs).
- Spring 2020 **Teaching assistant**, *Université de Rennes 1*, Rennes, France
Software-Hardware Interfaces. Instructor: Steven Derrien. 44 hours (recitations + labs).
- Fall 2019, Fall 2020 **Teaching assistant**, *Université de Rennes 1*, Rennes, France
Syntax-Driven Programming. Instructor: Sébastien Ferré. 16 hours (labs)
- Spring & Summer 2018 **REU Mentor**, *Colorado State University*, Fort Collins, CO, USA
Mentor of Sophomores + Juniors. Lead: Prof. Sanjay Rajopadhye.

Service

- 2022 **Artifact Evaluator**
ACM SIGPLAN 2022 International Conference on Compiler Construction (CC 2022).

Languages

- French Native
English Professional competence
Spanish Basic skills

Interests

- Sports I like practicing full nature sports like rock climbing, skiing or road biking.

Miscellaneous

- Driver's license Holder of a "B" French driver's license.