Executive summary: This project aims at confining malware samples for analysis in a secure environment while ensuring that these samples do not evade the analysis by detecting the environment. The specific challenge to address in this project is to support the analysis of malware samples that check the verisimilitude of network performances to detect analysis environments. To this end the project combines experiences from the Inria/Myriads team on simulation-based studies of distributed systems and from the DGA in cybersecurity and virtualization. The proposed internship focuses on studying a hardware virtualization implementation of the analysis environment.

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Required skills: Networks and VMs; System programming on Linux.

Appreciated but not mandated skills: Deep understanding of OSes; x86 assembly; Programming in C; Programming in Rust.

Context and Description
Malware analysts often rely on sandboxes to study malware and their interaction with the environment. In this context, the malware payload is executed in a virtual machine (VM) on top of a custom hypervisor. Various analysis tools can then safely analyze the malware execution from outside of the VM.

Attackers have however developed different evasion techniques, to detect sandboxes and hide their malicious behavior [1]. A class of evasion techniques relies on timing analysis. For instance, a malware can compare several time references to detect discrepancies that can be caused by the analysis environment. To the best of our knowledge, the currently known evasion techniques solely rely on comparing time references that are local to the VM (e.g. execution loops’ timings). Some sandboxes ensure that all local time references remain consistent to achieve stealthiness with that regard [2, 3, 4].

The TANSIV project focuses on making sandboxes stealth with respect to timing-analysis-based evasion techniques using network interactions as part of their time references. Such evasions are easy enough to implement to be considered as real threats from the analyst perspective. In the general case, a malware targeting a specific victim could compare its network environment with a fingerprint embedding the knowledge of the victim environment, including the local network size, performance, and geo-location. Similarly, with no prior knowledge of the victim environment, the evasion decision could be taken based on the performance of the communication to its Command and Control (C&C) servers as the network latency must follow approximately a distribution known in advance.

To add timing-based network-fingerprinting resistance to sandboxes, the TANSIV approach consists in interconnecting a sandbox and the network end-points of its environment with a scalable, performance-accurate, discrete-event network simulator. TANSIV decouples the wall-clock execution time from the time perceived inside the sandbox and its network environment, as the simulator coordinates the progression of time on all network end-points and when communications are delivered to their target end-points.
In a first step, we have implemented a prototype of TANSIV with the Qemu PC emulator and the SimGrid simulator [5]. The emulation mode of Qemu especially allows TANSIV to precisely control the progression of time in the VM. However, although some sandboxes are based on Qemu [6, 7], the emulation mode has 3 major drawbacks:

- wall-clock execution speed (as perceived by the human analyst) is much slower than in a real environment;
- emulation mode is easily detected from inside the VM (e.g. it is not cycle-accurate) and is a clear sign of a sandboxed execution;
- many sandboxes rather use hardware-assisted virtualization [8, 9], both for better speed and stealthiness.

To solve these issues the TANSIVTx approach should extend TANSIV with hardware-assisted virtualization, for which we must design solutions to precisely control the progression of time.

In this internship the student will:

1. learn the differences between full emulation and hardware virtualization,
2. extend TANSIV with a first approach of time control based on hardware virtualization,
3. experimentally evaluate this hardware-virtualization-based approach by comparison with the existing full-emulation-based TANSIV prototype.

Bibliography